## Fukuoka IST Selects Synopsys as its Primary EDA Supplier

Collaboration accelerates growth of rising industries in Japan

PRNewswire-FirstCall MOUNTAIN VIEW, Calif. (NASDAQ:SNPS)

MOUNTAIN VIEW, Calif., Oct. 15 PRNewswire-FirstCall/ -- Synopsys, Inc. (NASDAQ: SNPS), a world leader in software and IP for semiconductor design and manufacturing, today announced that Fukuoka Industry, Science & Technology Foundation (IST) has signed an expanded license agreement to establish Synopsys as its key EDA supplier. Located in the Fukuoka Prefecture of Japan, Fukuoka IST is a center for science and technology research that was established to support local emerging venture-capital firms through the entire business creation process and foster the development of local industries.

The newly expanded relationship provides Fukuoka IST with increased access to Synopsys' product portfolio, including Synopsys' Galaxy™ design platform featuring IC Compiler place-and-route technology, Design Compiler® synthesis, Power Compiler™ RTL Power Optimization, PrimeTime® timing analysis and TetraMAX® automatic test pattern generation (ATPG); Synopsys' Discovery™ verification platform featuring the VCS® functional verification tool and the HSPICE®, HSIM™ and HSIM-XA circuit simulators for analog and digital verification; and the DesignWare® System-level and Implementation Library for design and verification intellectual property (IP).

"Over the past five years we have utilized Synopsys' tools for joint industry-university-government research projects, and the results of these projects have enabled us to help grow local start-up companies and stimulate economic growth in the Fukuoka Prefecture," said Masato Tsuru, science and technology coordinator at Fukuoka IST. "We have chosen Synopsys as our key EDA supplier because of their solid track record of providing high quality solutions with great customer support. In addition, we know that Synopsys will help keep us on the forefront of technology and is a partner we can count on for the long run."

"By expanding our collaboration with Fukuoka IST, Synopsys solutions can be used to help drive additional technological growth and industry advancements in Japan," said John Chilton, senior vice president of marketing and strategic development at Synopsys. "We are fortunate to have partners such as Fukuoka IST who inspire us to stay on the cutting edge of technology by showing us what a positive impact it can have on local economies and quality of life."

## **About Synopsys**

Synopsys, Inc. (NASDAQ: SNPS) is a world leader in electronic design automation (EDA), supplying the global electronics market with the software, intellectual property (IP) and services used in semiconductor design and manufacturing. Synopsys' comprehensive, integrated portfolio of implementation, verification, IP, manufacturing and field-programmable gate array (FPGA) solutions helps address the key challenges designers and manufacturers face today, such as power and yield management, system-to- silicon verification and time-to-results. These technology-leading solutions help give Synopsys customers a competitive edge in bringing the best products to market quickly while reducing costs and schedule risk. Synopsys is headquartered in Mountain View, California, and has more than 60 offices located throughout North America, Europe, Japan, Asia and India. Visit Synopsys online at http://www.synopsys.com/.

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Editorial Contact: Yvette Huygen Synopsys, Inc. 650-584-4547 yvetteh@synopsys.com

Investor Contact: Lisa Ewbank Synopsys, Inc. 650-584-1901

SOURCE: Synopsys, Inc.

CONTACT: editorial, Yvette Huygen, +1-650-584-4547, yvetteh@synopsys.com, or investors, Lisa Ewbank, +1-650-584-1901, both of Synopsys, Inc.

Web site: http://www.synopsys.com/