Synopsys Enters Mixed-Signal Implementation Market With Galaxy Custom Designer

Next-generation Solution Provides Open, Unified Environment for Custom Design

MOUNTAIN VIEW, Calif., Sept. 22 /PRNewswire-FirstCall/ -- Synopsys, Inc. (NASDAQ: SNPS), a world leader in software and IP for semiconductor design and manufacturing, today unveiled its Galaxy Custom Designer™ solution, the industry's first modern-era mixed-signal implementation solution. Architected for productivity, Galaxy Custom Designer leverages Synopsys' Galaxy™ Design Platform to provide a unified solution for custom and digital designs, thereby enhancing designer efficiency. Galaxy Custom Designer delivers a familiar user interface while integrating a common use model for simulation, analysis, parasitic extraction and physical verification. It is the first-ever implementation solution built natively on the OpenAccess database for legacy designs as well as a new componentized infrastructure offering unprecedented openness and interoperability with process design kits (PDKs) from leading foundries.

"Our customers have long requested a modern alternative to the custom design solutions currently on the market," said Aart de Geus, chairman and CEO of Synopsys. "By starting with a state-of-the-art, open architecture and tightly coupling it to the Galaxy Design Platform as well as our analog/mixed-signal verification and IP solutions, Synopsys aims to do for custom design what we have done for digital implementation."

"Galaxy Custom Designer has an open architecture that natively supports interoperable PDKs which are an integral part of our Open Innovation Platform™, helping designers innovate in analog and full-custom design," said Fu-Chieh Hsu, vice president of Design & Technology Platform at TSMC. "We are collaborating with Synopsys to develop the industry's first interoperable PDK in 65 nanometer, a single PDK that supports multiple environments, including the latest innovations such as Custom Designer. We continue to work with Synopsys and the Interoperable PDK Library (IPL) Alliance to accelerate the deployment and adoption of interoperable PDK across the industry."

In addition to facilitating innovation with the open architecture, Galaxy Custom Designer dramatically enhances productivity by seamlessly bridging the gap between the historically disparate worlds of digital and custom design. Galaxy Custom Designer enables complete data transparency with Synopsys' IC Compiler physical implementation solution, allowing the exchange of vital information during floorplanning, placement, routing and final chip editing to reduce time-consuming design iterations.

"Our customers expect timely delivery and the utmost reliability from our advanced authentication security products," said Jeff Berkman, chief technology officer at Priva Technologies. "The smooth conversion of our mixed-signal security IP to Custom Designer, its inherent ease-of-use, and its intuitive interface enabled us to significantly reduce design iteration time and thus increase quality. We expect using Custom Designer for our future designs to greatly enhance our design productivity."

"The introduction of Custom Designer as part of the Galaxy Design Platform provides a complete cell-based and custom design capability that we are currently deploying on our next-generation HDRC® sensor array design," said Wolfram Klingler, senior manager, IC Design Tools at IMS Chips. "Custom Designer was easy to adopt, and offers significant productivity improvements over other solutions."

Built from the ground up, Galaxy Custom Designer was architected for productivity. Key modules include a schematic editor featuring on-canvas editing and dynamic net highlighting. The simulation environment provides a common use model allowing access to Synopsys simulators, including HSPICE®, HSIM™ XA, NanoSim® XA and WaveView Analyzer. The layout editor features a real-time preview of P-Cell parameter changes. In addition, the results from Hercules™ DRC/LVS and Star-RCXT™ parasitic extraction are dynamically available within Galaxy Custom Designer.

Galaxy Custom Designer is available now.

About Synopsys

Synopsys, Inc. (NASDAQ: SNPS) is a world leader in electronic design automation (EDA), supplying the global electronics market with the software, intellectual property (IP) and services used in semiconductor design and manufacturing. Synopsys' comprehensive, integrated portfolio of implementation, verification, IP, manufacturing and field-programmable gate array (FPGA) solutions helps address the key challenges designers and manufacturers face today, such as power and yield management, system-to-silicon verification and time-to-results. These technology-leading solutions help give Synopsys customers a competitive edge in bringing the best products to market quickly while reducing costs and schedule risk. Synopsys is headquartered in Mountain View, California, and has more than 60 offices located throughout North America, Europe, Japan, Asia and India. Visit Synopsys online at https://www.synopsys.com/.

Forward-Looking Statements

This press release contains forward-looking statements within the meaning of Section 27A of the Securities Act of 1933 and Section 21E of the Securities Exchange Act of 1934, including statements regarding the expected benefits and performance characteristics of Galaxy Custom Designer and interoperable PDKs. These statements are based on current expectations and beliefs. Actual results could differ materially from those described by these statements due to risks and uncertainties including, but not limited to, engineering difficulties, uncertainties attendant to any new product release, and other risks as identified in the section of Synopsys' Annual Report on Form 10-K for the fiscal year ended October 31, 2007, and subsequent forms 10-Q, entitled "Risk Factors."

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