MEDIA ADVISORY/ALERT: Synopsys Showcases DesignWare IP Solutions for PCI Express, USB and SATA at Intel Developer Forum 2008

PRNewswire-FirstCall
SAN FRANCISCO
(NASDAQ:SNPS)

SAN FRANCISCO, Aug. 14 /PRNewswire-FirstCall/ -- Synopsys, Inc. (NASDAQ: SNPS), a world leader in software and IP for semiconductor design and manufacturing, will be demonstrating its complete DesignWare® IP solutions for PCI Express®, USB and SATA at the Intel Developer Forum (IDF) in San Francisco, California on August 19, 2008. Furthermore, the DesignWare IP for PCI Express with PCI-SIG I/O Virtualization technology will be featured in the Intel Lab sessions at IDF.

IDF is one of the premier technical events for the release and exchange of new technology and information in the computer, network, wireless and communications realms.

WHAT: Synopsys will be showcasing its latest developments in DesignWare IP for PCI Express, USB and SATA at IDF in the PCI Express Technology Community, USB Community and the SATA-IO booth #326, respectively.

WHEN: August 19-21, 2008

WHERE: Moscone Center West, 800 Howard Street, San Francisco, CA 94103

EXHIBIT HOURS:
Tuesday, August 19, 6:00pm-8:00pm
Wednesday, August 20, 12:00pm-1:30-pm & 6:00pm-8:00pm
Thursday, August 21, 12:00pm-1:30pm

Synopsys Highlights at IDF:

PCI Express Technology Community - Synopsys Booth #726
-- DesignWare IP for PCI Express 2.0 Demonstration

Synopsys provides leading PCI Express IP solutions for a wide range of applications requiring single lane (x1) up to sixteen (x16) lane interfaces. Using the DesignWare PHY and digital controller, Synopsys will be demonstrating high performance data transfers at 5.0 Gbps using a PCI Express 2.0 based PC platform.

-- DesignWare PHY IP for PCI Express Demonstration

Synopsys will be demonstrating its silicon-proven DesignWare PHY for PCI Express 2.0, operating at 5.0 Gbps. Learn how the PHY exceeds the electrical specifications in areas of margin and receive sensitivity, providing more margin for the overall design. See how the unique built in diagnostics and ATE capabilities enable you to have visibility into the link performance and conduct at-speed production testing on a pure digital tester.

More info on the DesignWare IP solution for PCI Express.http://www.synopsys.com/pciexpress

PCI Express Technology Community - LeCroy Booth #729

-- PCI Express 2.0 I/O Virtualization with Synopsys' DesignWare IP and the LeCroy Protocol Test Card

Synopsys and LeCroy will be demonstrating the latest PCI Express 2.0 with PCI-SIG I/O Virtualization technology utilizing Synopsys' DesignWare IP and LeCroy's Summit T2-16 Analyzer and Protocol Test Card (PTC). The demonstration will focus on validating the correct behavior for PCI Express Single Root I/O Virtualization transactions by enabling and disabling Virtual Functions.

More info on the DesignWare IP for PCI Express 2.0 Solutionhttp://www.synopsys.com/pciexpress

More info on the LeCroy PTC for PCI Express 2.0 http://teledynelecroy.com/protocolanalyzer/

USB Community - Synopsys Booth #803
-- DesignWare IP for Hi-Speed USB On-the-Go and Wireless USB

Join Synopsys to see why designers are implementing high quality DesignWare USB IP for a wide range of portable electronics

and PC peripherals. See a live demonstration of the DesignWare Hi-Speed USB OTG and Wireless USB Device Controllers functioning as mass storage devices showing wired or wireless data transfers to the target device. Also, take this opportunity to talk to experts about the latest developments in DesignWare USB IP including USB Link Power Management (LPM), Hi-Speed Inter-Chip (HSIC) and USB 3.0.

More info on the DesignWare USB IP Solution.http://www.synopsys.com/products/designware/usb_solutions.html

For more information on the DesignWare IP portfolio, visit:http://www.synopsys.com/designware

About Synopsys

Synopsys, Inc. (NASDAQ: SNPS) is a world leader in electronic design automation (EDA), supplying the global electronics market with the software, intellectual property (IP) and services used in semiconductor design and manufacturing. Synopsys' comprehensive, integrated portfolio of implementation, verification, IP, manufacturing and field-programmable gate array (FPGA) solutions helps address the key challenges designers and manufacturers face today, such as power and yield management, system-to- silicon verification and time-to-results. These technology-leading solutions help give Synopsys customers a competitive edge in bringing the best products to market quickly while reducing costs and schedule risk. Synopsys is headquartered in Mountain View, California, and has more than 60 offices located throughout North America, Europe, Japan, Asia and India. Visit Synopsys online at http://www.synopsys.com/.

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