Alchip Adopts Synopsys' Eclypse Low Power Solution

Adoption for use in complex, high-volume designs marks latest milestone in long-standing collaboration

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MOUNTAIN VIEW, Calif. and HSINCHU, Taiwan, Aug. 12 /PRNewswire-FirstCall/ -- Synopsys, Inc. (NASDAQ: SNPS), a world leader in semiconductor design software, and Alchip Technologies, Inc., a leading fabless ASIC provider, today announced that Alchip has adopted Synopsys' Eclypse™ Low Power Solution as their preferred low power design methodology. This is the latest milestone in the companies' history of close collaboration on high-quality, high-volume, System-on-Chip (SoC) designs.

"Synopsys has been Alchip's premier EDA vendor since the very beginning," said Johnny Shen, vice president of engineering at Alchip. "We trust Synopsys' comprehensive suite of high-performance tools and their proactive technical support to tape out our complex SoCs on time, with a solid track record of first silicon success. Nearly all of the major tapeouts in Alchip's history were designed with Synopsys design tools. With the adoption of the Synopsys low power solution flow, combined with Alchip's low power design expertise and techniques, Alchip has high confidence in our ability to address the increasing complexity of low power design and verification in 65nm and below."

Synopsys' Eclypse Low Power Solution is the industry's most comprehensive suite of proven system-level, implementation, verification, and sign-off tools, intellectual property (IP), and services for low power chip development, reducing low power design risk and turnaround time. The Eclypse solution offers a streamlined, easy-to-use low power workflow that encompasses every phase of the design process. As a result, the Eclypse solution enables design teams to adopt advanced low power techniques to meet or beat power, area, speed and yield objectives while boosting productivity, reducing risk and ultimately delivering the highest-quality silicon. The advanced low power design techniques that customers can utilize in Eclypse flows include multi-supply voltage, multi-threshold (Vth), level shifter/ISO cell, power gating, dynamic voltage-frequency scaling (DVFS), IR analysis, static timing analysis and others.

"Synopsys has a long history of successful collaboration with Alchip, and our companies have worked closely together on numerous tapeouts," said George Zafiropoulos, vice president of Solutions Marketing for Synopsys. "Alchip's design requirements are growing increasingly complex, and their latest generation of devices is expected to include 31 power domains. This is an exponential increase in verification complexity compared to earlier designs. By adopting the Eclypse Low Power Solution, Alchip is well-positioned to meet these challenges while simultaneously reducing design turn-around time."

About Synopsys

Synopsys, Inc. (NASDAQ: SNPS) is a world leader in electronic design automation (EDA), supplying the global electronics market with the software, intellectual property (IP) and services used in semiconductor design and manufacturing. Synopsys' comprehensive, integrated portfolio of implementation, verification, IP, manufacturing and field-programmable gate array (FPGA) solutions helps address the key challenges designers and manufacturers face today, such as power and yield management, system-to-silicon verification and time-to-results. These technology-leading solutions help give Synopsys customers a competitive edge in bringing the best products to market quickly while reducing costs and schedule risk. Synopsys is headquartered in Mountain View, California, and has more than 60 offices located throughout North America, Europe, Japan, Asia and India. Visit Synopsys online at http://www.synopsys.com/.

About Alchip Technologies, Inc.

Alchip Technologies, Inc., headquartered in Taipei, Taiwan, is a provider of silicon design and manufacturing services for companies developing complex and high-volume system-on-chip (SoC) designs. Founded in 2002 by semiconductor veterans from Silicon Valley and Japan, Alchip provides services intended to enable customers to cost-effectively address increasing technological complexity of silicon design and the need to reduce time to market. Alchip targets customers that address rapidly growing and high-volume end markets, such as entertainment devices, mobile phones, HDTV, communications equipment, computers and other consumer electronic products. Alchip has provided design services on numerous complex SoC devices at 130nm, 90nm and 65nm advanced process technologies. Alchip has branch offices in the U.S. (Santa Clara, CA), Japan (Shin-Yokohama), China (Shanghai), and Taiwan (Hsinchu). Additional information about Alchip is available at http://www.alchip.com/.

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Sheryl Gulizia
Synopsys, Inc.
650-584-8635
sgulizia@synopsys.com

Lisa Gillette-Martin
MCA, Inc.
650-968-8900 ext. 115
lgmartin@mcapr.com

Steffi Lee
Alchip Technologies, Inc.
+886 2799 2318 x2305
Steffi@alchip.com

SOURCE: Synopsys, Inc.

CONTACT: Sheryl Gulizia of Synopsys, Inc., +1-650-584-8635, sgulizia@synopsys.com; or Lisa Gillette-Martin of MCA, Inc., +1-650-968-8900, ext. 115, lgmartin@mcapr.com, for Synopsys, Inc.; or Steffi Lee of Alchip Technologies, Inc., +886 2799 2318, ext. 2305, Steffi@alchip.com

Web site: http://www.synopsys.com/
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