Synopsys Discovery AMS Enables Analog Bits to Achieve 45nm SERDES Verification

HSPICE, HSIM and WaveView Analyzer Deliver First-silicon Success

MOUNTAIN VIEW, Calif., June 18 /PRNewswire-FirstCall/ -- Synopsys, Inc. (NASDAQ: SNPS), a world leader in software and IP for semiconductor design and manufacturing, today announced that Analog Bits, Inc. has deployed Synopsys' Discovery™ AMS, HSPICE® and HSIM™ simulators and WaveView Analyzer™ for sign-off verification of their new family of 45-nanometer (nm) 10-gigabit (Gbit) SERDES devices, which has enabled them to achieve first-silicon success. Analog Bits specializes in designing programmable interconnect solutions, such as multi-protocol SERDES, for systems-on-chips (SoCs) fabricated in nanometer CMOS logic processes.

"Analog Bits is the premier clocking IP supplier with a flawless track record of delivering first-time working silicon at the leading merchant semiconductor foundries and prestigious IDMs," said Mahesh Tirupattur, executive vice president of Analog Bits, Inc. "To maintain our record of excellence, we rely on the accuracy of the HSPICE® simulator, which is supported by silicon-verified models from each of our foundry partners. Synopsys' HSIM™ simulator provides us with high-performance functional verification of our complex analog/mixed-signal IP with 10-15X faster time to results. With the WaveView Analyzer™, we can thoroughly verify critical performance specifications of our 10Gbit SERDES, easily generating eye diagrams to measure jitter and signal integrity of our programmable interconnect solutions."

"At the latest 45-nanometer process node, the HSPICE simulator and HSIM simulator enable customers to accurately verify their analog and mixed-signal designs with the confidence they will be supported by leading semiconductor foundries," said Paul Lo, senior vice president and general manager of the Analog/Mixed-signal Group at Synopsys. "The combination of Synopsys' proven circuit simulation solution and the advanced WaveView Analyzer analysis, verification and debug tool will help enable our customers to deliver high- performance chips on time."

About Synopsys

Synopsys, Inc. (NASDAQ: SNPS) is a world leader in electronic design automation (EDA), supplying the global electronics market with the software, intellectual property (IP) and services used in semiconductor design and manufacturing. Synopsys' comprehensive, integrated portfolio of implementation, verification, IP, manufacturing and field-programmable gate array (FPGA) solutions helps address the key challenges designers and manufacturers face today, such as power and yield management, system-to-silicon verification and time-to-results. These technology-leading solutions help give Synopsys customers a competitive edge in bringing the best products to market quickly while reducing costs and schedule risk. Synopsys is headquartered in Mountain View, California, and has more than 60 offices located throughout North America, Europe, Japan, Asia and India. Visit Synopsys online at https://www.synopsys.com/.

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