Synopsys Extends Compute Platform Support to Include Solaris 10 OS-Based x86_64 Platforms

Broad support provided across its industry-leading electronic design automation solutions

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MOUNTAIN VIEW, Calif., June 5 /PRNewswire-FirstCall/ -- Synopsys, Inc. (NASDAQ: SNPS), a world leader in software and IP for semiconductor design and manufacturing, today announced broad support across its advanced electronic design automation (EDA) product lines for Sun Microsystems' Solaris™ 10 Operating System (OS) on x86_64-based systems. The combination of Synopsys' powerful EDA solutions and the proven Solaris 10 OS on robust x86_64 processors can provide significant productivity gains for engineering teams developing next-generation system-on-chip (SoC) designs. Synopsys products running on the Solaris 10 OS with x86_64 platforms are expected to provide engineering teams with a higher level of performance and productivity gains because these EDA tools and systems are able to provide the sophisticated solutions needed for the design and verification of today's SoCs.

"I am pleased to see Sun and Synopsys build on their long standing collaboration to help deliver world class electronic design automation solutions to customers," said Juan Carlos Soto, vice president of Global Market Development and Engineering at Sun Microsystems, Inc. "With this announcement, customers have even more hardware choices that leverage the Solaris OS and its proven scalability and performance to use Synopsys solutions to develop state of the art processors and ASICs."

"Our leading-edge EDA tool families' broad support for Solaris 10 on x86_64 processors offers our customers more choice and flexibility in the compute platforms available for SoC design," said Rich Goldman, vice president of Corporate Marketing and Strategic Market Development at Synopsys. "Our customers value the ability to choose optimal combinations of operating systems and hardware on which to execute our state-of-the-art product suites."

About Synopsys

Synopsys, Inc. (NASDAQ: SNPS) is a world leader in electronic design automation (EDA), supplying the global electronics market with the software, intellectual property (IP) and services used in semiconductor design and manufacturing. Synopsys' comprehensive, integrated portfolio of implementation, verification, IP, manufacturing and field-programmable gate array (FPGA) solutions helps address the key challenges designers and manufacturers face today, such as power and yield management, system-to-silicon verification and time-to-results. These technology-leading solutions help give Synopsys customers a competitive edge in bringing the best products to market quickly while reducing costs and schedule risk. Synopsys is headquartered in Mountain View, California, and has more than 60 offices located throughout North America, Europe, Japan, Asia and India. Visit Synopsys online at http://www.synopsys.com/.

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