Alert: Synopsys at Freescale Technology Forum

MOUNTAIN VIEW, Calif., June 2 /PRNewswire-FirstCall/ -- Synopsys, Inc. (NASDAQ: SNPS), a world leader in software and IP for semiconductor design and manufacturing, will highlight its complete virtual platform solutions and DesignWare® System-Level Library at the Freescale Technology Forum (FTF) in Orlando, Florida on June 16 - June 18, 2008. Synopsys is the leader in system-level solutions and connectivity IP, offering a complete solution comprised of models, tools and services. The FTF Americas event is one of the premier global technical events where the entire Freescale ecosystem can come together under one roof.

WHAT: Visit Synopsys at FTF and see how Synopsys' tools, models and services to deploy virtual platforms can help accelerate your software development. We will be showcasing Synopsys' VPMX31 virtual platform which models the Freescale i.MX31 Applications Development System. In addition, the DesignWare System-Level Library and Innovator solutions will also be demonstrated.

WHEN: June 16 - 18, 2008

WHERE: JW Marriott and Ritz-Carlton Conference Centers,
4040 Central Florida Parkway Orlando, FL 32837

FTF TECHNOLOGY LAB HOURS:
Monday, June 16, 5:00 pm - 9:00 pm
Tuesday, June 17, 12:30 pm - 2:00 pm & 5:30 pm - 8:30 pm
Wednesday, June 18, 11:45 am - 1:45 pm & 6:00 pm - 7:30 pm

SYNOPSYS HIGHLIGHTS AT FTF:

-- Synopsys Pedestal #1310 -- Complete virtual platform for the Freescale i.MX31 Application Development System
The VPMX31 Virtual Platform modeling the Freescale i.MX31 and the i.MX31L multimedia applications processor and i.MX31 Applications Development System improves software development productivity by providing access to a fast, full-function software emulation of your embedded device. For more information on the complete virtual platform solution the i.MX31 Applications Development System, please visit https://www.synopsys.com/virtualplatform.

-- Synopsys Pedestal #1311 -- DesignWare System-Level Library and Innovator
Synopsys' DesignWare® System-Level Library provides product development teams a broad portfolio of tool-independent transaction-level models (TLMs) for the creation of virtual platforms. Virtual platforms are fully functional software models of complete embedded systems developed, verified and delivered using Synopsys' Innovator, enabling pre-silicon software development and software-driven system validation. The ability to parallelize the hardware and software development effort through virtual platforms significantly reduces the product design cycle and speeds time to market. For more information please visit:
https://www.synopsys.com/designware-ip/interface-ip/usb.html .

-- Tuesday, June 17, 2008 at 11:30 Synopsys Presentation in Palazzo Salon D

Get to Market Early with Virtual Platforms for Pre-Silicon Software Development

Today, the development effort for software running on chip designs at 90 nm and beyond has already surpassed that of hardware development. This session introduces the advantages of using interoperable, transaction-level models (TLMs) based on OSCI's new TLM 2.0 standard to assemble virtual platforms. These SystemC-based TLMs are deployed early in the design cycle for software development, debug and system platform deployment. Using the actual example of the Freescale i.MX family for mobile communications this session details actual virtual platform use cases for pre-silicon software development and system validation. Synopsys' Innovator and DesignWare System-Level Library for the i.MX product family will be demonstrated.

For more information on FTF, please visit: http://www.freescale.com/ftf.

About Synopsys

Synopsys, Inc. (NASDAQ: SNPS) is a world leader in electronic design automation (EDA), supplying the global electronics market with the software, intellectual property (IP) and services used in semiconductor design and manufacturing. Synopsys' comprehensive, integrated portfolio of implementation, verification, IP, manufacturing and field-programmable gate array (FPGA) solutions helps address the key challenges designers and manufacturers face today, such as power and yield management, system-to-silicon verification and time-to-results. These technology-leading solutions help give Synopsys customers a competitive edge in bringing the best products to market quickly while reducing costs and schedule risk. Synopsys is headquartered in Mountain View, California, and has more than 60 offices located throughout North America, Europe, Japan, Asia and India. Visit Synopsys online at https://www.synopsys.com/.

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