HiSilicon Selects Synopsys as IP Vendor Of Choice for SoC Designs

High Quality and Breadth of DesignWare IP Portfolio Key Selection Criteria for HiSilicon

MOUNTAIN VIEW, Calif., May 13 /PRNewswire-FirstCall/ -- Synopsys, Inc. (NASDAQ: SNPS), a world leader in software and IP for semiconductor design and manufacturing, today announced that HiSilicon Technologies, an IC design company for communication networks and digital media, has chosen Synopsys' DesignWare® IP as the de-facto standard for HiSilicon's system-on-chip (SoC) designs. The multi-year agreement gives HiSilicon access to Synopsys' broad portfolio of leading DesignWare IP such as PCI Express, USB and DDR protocols, which enable HiSilicon to reduce risk and speed time-to-market for their communication infrastructure and consumer electronics designs.

"After evaluating several competitive offerings, we selected Synopsys because of its established leadership in delivering high-quality IP, which has been silicon-proven in a wide range of high-performance SoC designs," said Teresa He, senior vice-president at HiSilicon Technologies. "Combined with Synopsys' breadth of complete solution offerings and excellent technical support, this made Synopsys' DesignWare IP an obvious choice for us."

"As the leading provider in connectivity IP, we continue to provide customers such as HiSilicon with comprehensive, high quality solutions that reduce design risk and enable them to develop complex chips on schedule," said Joachim Kunkel, vice president and general manager of the Solutions Group at Synopsys. "We appreciate HiSilicon's trust in Synopsys and welcome the opportunity to help them ramp their SoC designs to volume production faster."

About DesignWare IP

Synopsys offers a broad portfolio of high-quality, silicon-proven digital, mixed-signal and verification IP for system-on-chip designs. As the leading provider of connectivity IP, Synopsys delivers the industry's most complete solutions for widely used protocols such as USB, PCI Express, SATA, Ethernet and DDR. In addition to connectivity IP, Synopsys offers SystemC transaction level models to build virtual platforms for rapid, pre-silicon development of software. When combined with a robust IP development methodology, extensive investment in quality and comprehensive technical support, DesignWare IP enables designers to accelerate time-to-market and reduce integration risk. For more information on DesignWare IP, visit https://www.synopsys.com/designware

About Synopsys

Synopsys, Inc. is a world leader in EDA software for semiconductor design. The company delivers technology-leading semiconductor design and verification platforms and IC manufacturing software products to the global electronics market, enabling the development and production of complex systems-on-chips (SoCs). Synopsys also provides intellectual property and design services to simplify the design process and accelerate time-to-market for its customers. Synopsys is headquartered in Mountain View, California and has offices in more than 60 locations throughout North America, Europe, Japan and Asia. Visit Synopsys online at https://www.synopsys.com/.

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