Radiospire Standardizes on Synopsys VCS and VMM Methodology for Next- Generation AirHook Chipset Designs

VCS Provides Higher Productivity by Delivering 40 Percent Faster Performance

MOUNTAIN VIEW, Calif., Feb. 14 /PRNewswire-FirstCall/ -- Synopsys, Inc. (NASDAQ: SNPS), a world leader in software and IP for semiconductor design and manufacturing, today announced that Radiospire Networks, Inc. has standardized on Synopsys' VCS® functional verification solution for SystemVerilog-based verification of their next-generation AirHook chipsets used for wireless transmission of high-definition (HD) video and audio. The VCS functional verification solution enabled Radiospire to architect an advanced constrained- random, coverage-driven environment to extensively verify the AirHook designs while meeting their stringent product development schedule. The state-of-the- art verification environment using the VCS solution's powerful bug-finding technologies allowed Radiospire to create a highly reusable verification flow for future design revisions.

"After a careful study of competing solutions, we decided to use Synopsys' VCS solution for SystemVerilog-based verification of our AirHook chipsets," said Richard Hollingsworth, vice president of Operations, at Radiospire. "VCS delivered 40 percent more performance, and reduced the memory footprint by half. We expect new versions of VCS to further reduce verification time of future-generation designs."

Comprised of a digital baseband processor, an analog front-end, and radio, Radiospire's AirHook chipsets are purpose-built to be embedded in today's advanced consumer electronics products. Using 1.7GHz of bandwidth, AirHook chipsets offer the high-quality, cost-effective solution for wireless HD video and audio.

Synopsys' technology-leading VCS SystemVerilog solution is used by hundreds of project teams around the world. The VCS functional verification solution provides full support for all widely used languages, including Verilog, VHDL, SystemC, and OpenVera. The third-generation parallel constraints solver in VCS uses multiple engines to find highly efficient solutions that deliver higher coverage in any given time.

"The robustness and maturity of Synopsys' customer-proven SystemVerilog verification solution based on VCS and the VMM methodology makes it the preferred choice for innovative semiconductor system companies like Radiospire," said Swami Venkat, senior director of Verification Marketing at Synopsys. "In addition to its many strengths, the VMM methodology captures industry best practices for enabling efficient verification reuse. It builds upon Synopsys' high-performance VCS NTB technology to significantly improve verification productivity and predictability."

About Synopsys

Synopsys, Inc. (NASDAQ: SNPS) is a world leader in electronic design automation (EDA) software for semiconductor design. The company delivers technology-leading system and semiconductor design and verification platforms, IC manufacturing and yield optimization solutions, semiconductor intellectual property and design services to the global electronics market. These solutions enable the development and production of complex integrated circuits and electronic systems. Through its comprehensive solutions, Synopsys addresses the key challenges designers and manufacturers face today, including power management, accelerated time to yield and system-to-silicon verification. Synopsys is headquartered in Mountain View, California, and has more than 60 offices located throughout North America, Europe, Japan and Asia. Visit Synopsys online at https://www.synopsys.com/.

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