Synopsys CFO Brian Beattie to Speak at D.A. Davidson Electronic Systems Design Conference

MOUNTAIN VIEW, Calif., Feb. 13 /PRNewswire-FirstCall/ -- Synopsys, Inc. (NASDAQ: SNPS), a world leader in software and IP for semiconductor design and manufacturing, today announced that Brian Beattie, CFO, will speak at the D.A. Davidson Electronic Systems Design Conference in Denver, Colorado.

This event will be broadcast live on the Internet via the Synopsys corporate website at https://www.synopsys.com/corporate/invest/invest.html on Friday, February 22, 2008 at 11:00 a.m. MT (1:00 p.m. ET). To access the live webcast presentation, please go to the website at least 10 minutes early to register and to download any necessary audio software. The webcast replay of the presentation can be accessed at the Synopsys corporate website within approximately 5 minutes following the conclusion of the live event.

About Synopsys

Synopsys, Inc. (NASDAQ: SNPS) is a world leader in electronic design automation (EDA) software for semiconductor design. The Company delivers technology-leading system and semiconductor design and verification platforms, IC manufacturing and yield optimization solutions, semiconductor intellectual property and design services to the global electronics market. These solutions enable the development and production of complex integrated circuits and electronic systems. Through its comprehensive solutions, Synopsys addresses the key challenges designers and manufacturers face today, including power management, accelerated time to yield and system-to-silicon verification. Synopsys is headquartered in Mountain View, California, and has more than 60 offices located throughout North America, Europe, Japan and Asia. Visit Synopsys online at https://www.synopsys.com/.

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