Synopsys' DesignWare IP Passes Certified Wireless USB Testing From USB-IF

Certified Wireless USB Logo Increases Customer Confidence in IP Compatibility and Interoperability

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MOUNTAIN VIEW, Calif., Nov. 26 /PRNewswire-FirstCall/ -- Synopsys, Inc. (NASDAQ: SNPS), a world leader in software and IP for semiconductor design and manufacturing, today announced that Synopsys' DesignWare® Wireless USB Device IP (https://www.synopsys.com/designware-ip/interface-ip/usb.html) has passed the USB Implementers Forum's (USB-IF) Certified Wireless USB Testing and earned the certification logo. Certification helps ensure interoperability with other Wireless USB enabled products in the market, thus minimizing risk and reducing time-to-market for designers integrating Wireless USB interfaces into their system-on-chips. This most recent certification reinforces Synopsys' leadership position in providing users with IP that is interoperable and low-risk.

The DesignWare Wireless USB Device IP is designed to the WiMedia Alliance Ultra-wideband (UWB) Common Radio Platform and includes a WiMedia MAC-PHY interface for interoperability with WiMedia UWB PHYs such as those from Alereon and Realtek. To improve power conservation in next-generation portable electronics applications, the DesignWare Wireless USB Device IP provides optimized power management functions with dual power rails. This allows the design to shut down power to sections that are not in use, thus lowering overall power consumption. In order to satisfy IP designers' complete protocol requirements, Synopsys offers a comprehensive Wireless USB IP portfolio including the device, WHCI host and dual-role device.

"It is great to see companies like Synopsys, which have been longtime supporters of USB technologies, be among the first to achieve Wireless USB certification for their DesignWare Wireless USB Device IP," said Jeff Ravencraft, USB-IF president. "The availability of certified IP is an important milestone in the industry and offers solutions for companies looking to develop high quality, interoperable products and get them to market quickly."

"Our customers continually tell us that high quality and interoperability are key elements in helping them achieve product success and reduce time-to-market," said John Koeter, group director for IP Marketing at Synopsys. "Certification is a very important part of the interoperability process, which is why we continue to provide designers with the most certified USB IP in the world. As the Certified Wireless USB standard continues to mature, designers can depend on Synopsys to provide them with this critical IP."

Synopsys is a technical contributor to the Certified Wireless USB Specification. For more information on the latest in USB go to http://synopsysoc.org/tousbornottousb/index.php.

Availability

The DesignWare Wireless USB Device, WHCI Host and Dual Role Device IP are available now.

About DesignWare IP

Synopsys offers a broad portfolio of high-quality, silicon-proven digital, mixed-signal and verification IP for system-on-chip designs As a leading provider of connectivity IP, Synopsys delivers the industry's most complete solutions for widely used protocols such as USB, PCI Express, SATA, Ethernet and DDR. In addition to connectivity IP, Synopsys offers SystemC transaction level models to build virtual platforms for rapid, pre-silicon development of software. When combined with a robust IP development methodology, extensive investment in quality and comprehensive technical support, DesignWare IP enables designers to accelerate time-to-market and reduce integration risk. For more information on DesignWare IP, visit http://www.synopsys.com/designware

About Synopsys

Synopsys, Inc. (NASDAQ: SNPS) is a world leader in electronic design automation (EDA) software for semiconductor design. The company delivers technology-leading system and semiconductor design and verification platforms, IC manufacturing and yield optimization solutions, semiconductor intellectual property and design services to the global electronics market. These solutions enable the development and production of complex integrated circuits and electronic systems. Through its comprehensive solutions, Synopsys addresses the key challenges designers and manufacturers face today, including power management, accelerated time to yield and system-to-silicon verification. Synopsys is headquartered in Mountain View,

California, and has more than 60 offices located throughout North America, Europe, Japan and Asia. Visit Synopsys online at http://www.synopsys.com/.

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Editorial Contact:

Yvette Huygen
Synopsys, Inc.
650-584-4547
yvetteh@synopsys.com

Andrea Zils
MCA
650-968-8900
azils@mcapr.com

SOURCE: Synopsys, Inc.

CONTACT: Yvette Huygen of Synopsys, Inc., +1-650-584-4547, yvetteh@synopsys.com; or Andrea Zils of MCA, +1-650-968-8900, azils@mcapr.com, for Synopsys, Inc.

Web site: http://www.synopsys.com/