ARM and Synopsys Feature Special Low Power Events at ARM Developers' Conference

MOUNTAIN VIEW, Calif., Sept. 27 /PRNewswire-FirstCall/ --

WHAT: Synopsys, Inc. (NASDAQ: SNPS), a world leader in semiconductor design software, and ARM [(LSE: ARM); (NASDAQ: ARMHY)] invite you to attend two special events at ARM Developers' Conference, October 2-4 at the Santa Clara Convention Center: a Low Power Industry Experts Panel and special evening reception with the Full Disclosure Blues Band.

Low Power Panel: Shaken or Stirred by Low Power Design Challenges?
Wednesday, October 3, 4:00 - 5:15pm

Authors of the Low Power Methodology Manual (LPMM), the industry-recognized low power experts from ARM and Synopsys will share case studies and step-by-step recommendations to achieve low-power silicon success. The panel will discuss solutions to static and dynamic power challenges, including MTCMOS state retention power gating, dynamic voltage and frequency scaling, as well as power network design and analysis. For more information about the panel and to register, please visit: https://www.synopsys.com/cgi-bin/devcon/reg1.cgi

Low Power Cocktail Party
Wednesday, October 3, 5:00 - 7:00pm
ARM and Synopsys are pleased to sponsor the evening cocktail reception. This signature event will allow attendees to mingle with exhibitors while enjoying food and beverages, and is open to all exhibit floor registrants.

Highlights include:

- -- Low Power Methodology Manual authors Michael Keating, David Flynn, Rob Aitken and Alan Gibbons will be signing copies of their recently published book, which has already sold more than 1500 copies since its release in late July.
- -- The EDA industry's Full Disclosure Blues Band will be performing, bringing together band members from across the industry and around the world.

WHEN: October 2-4, 2007

WHERE: Santa Clara Convention Center, 5001 Great America Parkway, Santa Clara, CA 95054

ARM DEVELOPERS' CONFERENCE EXHIBIT HOURS:
Wednesday, October 3, 10:00 AM - 7:00 PM
Thursday, October 4, 9:00 AM - 4:00 PM

ADDITIONAL SYNOPSYS HIGHLIGHTS AT ARM DEVELOPERS' CONFERENCE:
Synopsys will present the latest design flows and currently planned new feature highlights in Synopsys Galaxy™ Design Platform, Discovery™ Verification Platform, DesignWare® connectivity IP and Virtual Platforms for rapid implementation of high-performance, low-power ARM processor-based products.

Visit the Synopsys Booth, Booth #613 for live demonstrations on Synopsys' Low Power Solution and ARM processor-based Virtual Platforms.

The Synopsys Design Center, Booth #613, a private classroom on the exhibit floor, will feature the following sessions:

1. Achieving Low Power for High-performance PHY IP
2. Increasing Designer Productivity for ARM Processor-based High-performance Designs with IC Compiler: ARM Cortex™-A8 Processor Case Study
3. Aggressive Low Power Implementation of Synthesizable ARM Microprocessors
4. Streamlining SoC Design from ESL through GDSII with SPIRIT Compatible IP
5. Using Galaxy Design Platform to Manage Power in 45nm and 65nm Designs

6. Maximize Virtual Platform ROI by Efficient Model Development

To register for the Synopsys Design Center and Low Power Panel, or for more information on the ARM-Synopsys exhibits at the ARM Developers' Conference, please visit: https://events.synopsys.com/

For more information on ARM Developers' Conference, please visit: http://www.rtcgroup.com/arm/2007/

For More Information on Synopsys at ARM DevCon: Visit the Synopsys ARM Developers' Conference home page at https://www.synopsys.com/news/events/conference07/arm_devcon07.html.

For more information on ARM-Synopsys collaborations, please visit https://www.synopsys.com/arm.

For more information on the Low Power Methodology Manual, please visit http://www.lpmm-book.org/.

About Synopsys

Synopsys, Inc. (NASDAQ: SNPS) is a world leader in electronic design automation (EDA) software for semiconductor design. The company delivers technology-leading system and semiconductor design and verification platforms, IC manufacturing and yield optimization solutions, semiconductor intellectual property and design services to the global electronics market. These solutions enable the development and production of complex integrated circuits and electronic systems. Through its comprehensive solutions, Synopsys addresses the key challenges designers and manufacturers face today, including power management, accelerated time to yield and system-to-silicon verification. Synopsys is headquartered in Mountain View, California, and has more than 60 offices located throughout North America, Europe, Japan and Asia. Visit Synopsys online at https://www.synopsys.com/.

About ARM

ARM designs the technology that lies at the heart of advanced digital products, from wireless, networking and consumer entertainment solutions to imaging, automotive, security and storage devices. ARM's comprehensive product offering includes 16/32-bit RISC microprocessors, data engines, graphics processors, digital libraries, embedded memories, peripherals, software and development tools, as well as analog functions and high-speed connectivity products. Combined with the company's broad Partner community, they provide a total system solution that offers a fast, reliable path to market for leading electronics companies. More information on ARM is available at http://www.arm.com/.

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Synopsys Editorial Contacts
Yvette Huygen
Synopsys, Inc.
650-584-8635
yvetteh@synopsys.com

Ellen Van Etten
MCA
970-778-6094
evanetten@mcapr.com

ARM Editorial Contacts
ARM PRESS OFFICE: +44 208 846 0797

Haran Ramachandran	Michelle Spencer	Claudia Natalia
Text 100	ARM	ARM
+44 20 8846 0727	+44 1628 427 780	+1 408 548 3172
haranr@text100.co.uk	michelle.spencer@arm.com	claudia.natalia@arm.com

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CONTACT: Synopsys Editorial Contacts, Yvette Huygen of Synopsys, Inc., +1-650-584-8635, yvetteh@synopsys.com, or Ellen Van Etten of MCA, +1-970-778-6094, evanetten@mcapr.com; or ARM Editorial Contacts, ARM PRESS OFFICE: +44 208 846 0797, or Haran Ramachandran of Text 100, +44 20 8846 0727, haranr@text100.co.uk, or Michelle Spencer of ARM,

+44 1628 427 780, michelle.spencer@arm.com, or Claudia Natalia of ARM,
+1-408-548-3172, claudia.natalia@arm.com

Web site: https://www.synopsys.com/