Synopsys HSPICE Simulator and SiSoft Quantum-SI Combine to Deliver Robust Signal Integrity Analysis Solution

Integrated Solution Provides Fast, Accurate High-Speed Interface Analysis

MOUNTAIN VIEW, Calif. and MAYNARD, Mass., Sept. 12 /PRNewswire-FirstCall/ -- Synopsys, Inc. (NASDAQ: SNPS), a world leader in semiconductor design software, and Signal Integrity Software, Inc. (SiSoft™), a leading provider of integrated timing and signal integrity solutions for high-speed digital design, today jointly announced the integration of SiSoft's Quantum-SI™ tool and Synopsys' HSPICE® simulation solution to deliver robust timing and signal integrity analysis for package and printed circuit board (PCB) design.

SiSoft's Quantum-SI tool targets high-speed digital interface design, allowing users to rapidly achieve design closure. Quantum-SI provides an environment around HSPICE that automates the analysis of entire interfaces for both timing and signal integrity, providing accurate predictions of system-level voltage and timing margins in a fraction of the time required by traditional methods. Quantum-SI provides exhaustive pre-layout and post-layout analysis capabilities that allow designers to rapidly analyze the consequences of potential design changes and adjust the design's implementation to optimize margins. HSPICE provides fast and high-accuracy signal integrity simulation through advanced transistor, Verilog-A, IBIS, W-element and S-element modeling support. Signal integrity analysis can be a compute-intensive task, and Quantum-SI leverages computer server farms to run HSPICE's high-accuracy simulations in parallel, increasing analytical throughput.

"HSPICE is widely recognized as the preferred sign-off tool for signal integrity simulations," noted Todd Westerhoff, vice president of Software Products for SiSoft. "Quantum-SI extends HSPICE to provide a comprehensive design and analysis environment for pre- and post-route analysis that allows customers to seamlessly mix IBIS and transistor-level models. Our customers have benefited greatly from HSPICE's accuracy and scalability for detailed signal integrity and power modeling applications."

"Package and board designers appreciate HSPICE's unique ability to perform accurate signal integrity simulation from silicon-to-package-to-board-to- backplane," said Bijan Kiani, vice president of Product Marketing at Synopsys. "The integration with Quantum-SI effectively extends HSPICE to provide a comprehensive design analysis and signal integrity regression environment for today's most demanding design problems."

SiSoft and Synopsys are committed to working together to continually improve the integration between their respective toolsets.

About SiSoft

SiSoft is the leading provider of integrated timing and signal integrity solutions for high-speed digital system design. SiSoft ensures its tools remain at the forefront of high-speed design by providing advanced high-speed consulting services including model development, I/O characterization, package design/analysis and system-level interconnect analysis. SiSoft design consultants utilize SiSoft's own commercial software to address some of the industry's most demanding design problems, helping ensure software product quality and functionality. SiSoft is headquartered in Maynard, Massachusetts with distributors worldwide. More information is available online at http://www.sisoft.com/.

About Synopsys

Synopsys, Inc. (NASDAQ: SNPS) is a world leader in electronic design automation (EDA) software for semiconductor design. The Company delivers technology-leading system and semiconductor design and verification platforms, IC manufacturing and yield optimization solutions, semiconductor intellectual property and design services to the global electronics market. These solutions enable the development and production of complex integrated circuits and electronic systems. Through its comprehensive solutions, Synopsys addresses the key challenges designers and manufacturers face today, including power management, accelerated time to yield and system-to-silicon verification. Synopsys is headquartered in Mountain View, California, and has more than 60 offices located throughout North America, Europe, Japan and Asia. Visit Synopsys online at https://www.synopsys.com/.

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