Teradici Corporation Selects Synopsys to Help Deliver Breakthrough PC-Over-IP Technology

Synopsys Tools, IP and Services Enable First-Silicon Success for Teradici's Complex SoC

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MOUNTAIN VIEW, Calif., July 9 /PRNewswire-FirstCall/ -- Synopsys, Inc. (NASDAQ: SNPS), a world leader in semiconductor design software, today announced that Teradici Corp. has achieved first-time silicon success utilizing Synopsys' Galaxy™ Design Platform products, Discovery™ Verification Platform products, DesignWare® silicon intellectual property (IP) and Synopsys Professional Services on their first multi-million-gate system-on-chip (SoC). This unique PC-over-IP™ SoC technology spans the enterprise network, delivering breakthrough innovation in computer display compression to emulate a true personal computer (PC) experience over internet protocol networks. Synopsys was able to help Teradici deliver the complex SoC quickly, within the tight market window, and achieve silicon success.

"We received first samples in early December, and immediately had critical high-speed interconnect IP and processor cores running successfully. Within another week, our most complex logic was fully operational," said Maher Fahmi, vice president of Silicon Engineering at Teradici. "With the help of Synopsys' industry-leading tools, IP and design services, Teradici was able to achieve first-silicon success."

Synopsys' consultants worked cooperatively with Teradici's engineers using tools from Synopsys' Galaxy Design Platform and Discovery Verification Platform, as well as DesignWare silicon IP including PCI Express and USB 2.0 digital controllers and PHY IPs. Teradici adopted Synopsys' Pilot Design Environment for complete IP integration and RTL-to-GDSII physical design assistance through tapeout.

For Teradici, a critical component in selecting a vendor was selecting the right IP. Synopsys provided Teradici with the industry's lowest-power, smallest-footprint PCI Express endpoint controller and PHY IP. In addition, Synopsys was the only company offering a native 4-port USB 2.0 PHY IP in the required target process, an integrated solution that saved both chip area and power consumption. Then, Synopsys' expert design services consultants assisted with the physical design implementation and provided Teradici post-silicon validation in just two weeks.

"Innovative start-ups like Teradici are able to entrust their critical 'go to market' products with Synopsys' tools, IP and design services," said John Koeter, senior director of marketing for IP and services at Synopsys. "As validated by Teradici's milestone achievement, Synopsys offers a total solution to help customers achieve predictable success."

About Synopsys

Synopsys, Inc. (NASDAQ: SNPS) is a world leader in electronic design automation (EDA) software for semiconductor design. The company delivers technology-leading system and semiconductor design and verification platforms, IC manufacturing and yield optimization solutions, semiconductor intellectual property and design services to the global electronics market. These solutions enable the development and production of complex integrated circuits and electronic systems. Through its comprehensive solutions, Synopsys addresses the key challenges designers and manufacturers face today, including power management, accelerated time to yield and system-to-silicon verification. Synopsys is headquartered in Mountain View, California, and has more than 60 offices located throughout North America, Europe, Japan and Asia. Visit Synopsys online at http://www.synopsys.com/.

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