

Synopsys and Synplicity Establish Alliance to Advance High-Performance ASIC Verification

Companies Leverage Unique Core Competencies to Further Hardware-assisted ASIC Verification

MOUNTAIN VIEW AND SUNNYVALE, Calif., May 24, 2007 — Synopsys (Nasdaq: SNPS), a world leader in semiconductor design software, and Synplicity, Inc. (Nasdaq: SYNP), a leading supplier of software for the design and verification of semiconductors, today announced the signing of a joint marketing agreement intended to improve verification flows for their mutual customers. Under terms of the agreement, the companies intend to work together on next-generation, high-performance verification solutions for ASIC designers. Targeting FPGA-based prototyping environments, Synopsys and Synplicity plan to demonstrate Synopsys' VCS® RTL functional verification solution working seamlessly with Synplicity's new Identify® Pro software with TotalRecall™ technology, a tool adding full visibility to FPGA-based prototyping systems. A press release announcing Synplicity's Identify Pro software was distributed today (Synplicity Delivers Breakthrough ASIC Verification Solution).

"Synplicity and Synopsys are focused on improving verification flows for the ASIC design community," said George Zafiropoulos, vice president of Marketing, Discovery Verification Platform, Synopsys. "Working together we are able to deliver a solution that can increase productivity for FPGA-based prototyping of ASIC designs. The Synopsys VCS solution and the Synplicity Identify Pro product will enable our mutual customers to more quickly perform interactive debugging of FPGA-based prototypes."

"We believe the collaboration between Synplicity and Synopsys will result in innovative new hardware-assisted verification flows," said Joe Gianelli, vice president, Business Development and Strategic Alliances, Synplicity. "We have been working with Synopsys during the past six months to ensure that our new Identify Pro software and Synopsys' industry-leading VCS solution work seamlessly together to provide full visibility for high-performance ASIC prototyping flows. Using this flow will enable designers to find and debug design issues that are otherwise only caught using live system stimulus."

The Synopsys-Synplicity joint marketing agreement grants both companies licenses of their respective products to create a highly tested flow between their respective tools. Under the terms of the agreement, Synopsys and Synplicity intend to explore additional capabilities from each company's product portfolio to advance their combined, next-generation verification solutions.

Synplicity and Synopsys will be exhibiting at the 2007 Design Automation Conference, June 4-7 in San Diego, California. Synopsys will be in booth # 5278. Synplicity will be in booth # 4278 and plans a demonstration of the Synplicity-Synopsys integrated ASIC prototype flow.

About Synopsys

Synopsys, Inc. is a world leader in EDA software for semiconductor design. The company delivers technology-leading semiconductor design and verification platforms and IC manufacturing software products to the global electronics market, enabling the development and production of complex systems-on-chips (SoCs). Synopsys also provides intellectual property and design services to simplify the design process and accelerate time-to-market for its customers. Synopsys is headquartered in Mountain View, California and has offices in more than 60 locations throughout North America, Europe, Japan and Asia. Visit Synopsys online at <https://www.synopsys.com>.

About Synplicity

Synplicity®, Inc. (Nasdaq: SYNP) is a leading supplier of innovative software solutions that enable the rapid and effective design of Programmable Logic Devices (FPGAs, PLDs and CPLDs) that serve a wide range of communications, military/aerospace, consumer, semiconductor, computer, and other electronic systems markets. Synplicity's tools provide outstanding performance, cost and time-to-market benefits by simplifying, improving and automating key design planning, logic synthesis, physical synthesis and verification functions for FPGA, FPGA-based ASIC verification, and DSP designers. Synplicity is the number one supplier of FPGA synthesis solutions and has been rated #1 in customer satisfaction since 2004 in EE Times' Annual FPGA Customer Survey. Synplicity products support industry-standard design languages (VHDL and Verilog) and run on popular platforms. The company operates in over 20 facilities worldwide and is headquartered in Sunnyvale, California. For more information visit <http://www.synplicity.com>.

Forward-Looking Statements

This press release contains forward-looking statements including, but not limited to, statements regarding the performance, achievements and benefits of the joint development efforts between Synopsys and Synplicity and the ASIC verification solution combining their respective products. In some cases, you will be able to identify forward-looking statements by terminology

such as “may,” “will,” “should,” “expects,” “can,” “believes” or the negative of these terms or other comparable terminology. These statements are only predictions and involve known and unknown risks, uncertainties and other factors that may cause the actual results to differ materially from the forward-looking statements, including the continued collaboration between the two companies, the performance and benefits of the products relative to relevant industry methods and standards, the ability of Synopsys and Synplicity to devote the necessary resources for the joint development arrangement, design flaws, design difficulties or other risks identified in the two companies’ respective filings with the Securities and Exchange Commission. Although both companies believe that the expectations reflected in the forward-looking statements are reasonable, they cannot guarantee the future performance or achievements of their software. In addition, neither company assumes responsibility for the accuracy or completeness of these forward-looking statements. Both companies disclaim any obligation to update information contained in any forward-looking statement.

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