Synopsys DesignWare IP Enables Full-Service SoC Design Foundry for Global UniChip

Full Portfolio of Synopsys' IP Expands GUC's Deep Submicron Offering

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Synopsys, Inc. (NASDAQ: SNPS), a world leader in semiconductor design software, and Global UniChip (GUC), a leader in advanced full-service design foundry, today announced that GUC has chosen Synopsys' DesignWare® intellectual property (IP) for use in GUC's complex system-on-chip (SoC) designs. The multi-year agreement gives GUC access to the wide portfolio of industry-leading DesignWare connectivity IP for standards such as PCI Express™ and USB protocols, as well as Synopsys' DesignWare Library building block and verification IP.

"We chose Synopsys because of its leadership in connectivity IP and its broad range of IP offerings," said Jim Lai, COO and president at Global UniChip. "As a leading design foundry, Global UniChip only trusts IP that has been tested and implemented on a wide range of complex SoC designs. Synopsys' reputation and history of deep submicron design success ensures us that their IP is a sound investment that will fit seamlessly into our design flow."

"We are proud that a world class SoC design foundry such as GUC chose the DesignWare portfolio of quality IP for their cutting-edge submicron designs," said Guri Stark vice president of Marketing, Solutions Group at Synopsys. "DesignWare silicon-proven IP will help enable GUC and their customers to reduce risk, cost, and time to market of their SoC designs."

Synopsys and GUC will be participating at the worldwide TSMC Symposiums beginning April 26, 2005 in San Jose, California.

About DesignWare IP

Synopsys DesignWare IP enables designers to more cost effectively create and verify complex SoCs, ASICs and FPGAs. The broad IP portfolio includes industry-leading connectivity IP cores and Verification IP (e.g., USB 1.1, 2.0, OTG and USB PHYs, PCI, PCI-X®, PCI Express™, PCI Express PHYs, Ethernet, SATA, IEEE 1394, I2C), AMBA™ on-chip bus (logic, peripherals, verification IP) complete memory solution (e.g., memory controllers, memory BIST and memory models), high-speed datapath components, microcontrollers (8051, 6811) and Star IP processors and DSP core (e.g., IBM PowerPC® 440, Infineon C166™S and TriCore®1, MIPS32™ 4KE™, NEC V850E™, Philips CoolFlux™ DSP). When combined with our robust IP development methodology, extensive investment in quality and comprehensive worldwide technical support, DesignWare IP gives designers a faster, more predictable and lower-risk path to chip success.

For a complete directory of Synopsys IP visit: www.synopsys.com/ipdirectory

For more information on DesignWare IP, visit: www.designware.com or call 1-877-4BEST-IP

About Synopsys

Synopsys, Inc. is a world leader in EDA software for semiconductor design. The company delivers technology-leading semiconductor design and verification platforms and IC manufacturing software products to the global electronics market, enabling the development and production of complex systems-on-chips (SoCs). Synopsys also provides intellectual property and design services to simplify the design process and accelerate time-to-market for its customers. Synopsys is headquartered in Mountain View, California and has offices in more than 60 locations throughout North America, Europe, Japan and Asia. Visit Synopsys online at http:/www.synopsys.com .

About GUC

GUC, a dedicated one-stop, full-service SoC design foundry, provides total solutions from silicon-proven IPs to challenging complex time-to-market SoC design, production and testing services. With state of the art EDA tools, proven very deep submicron design flow and expert engineers, GUC provides customers with the best quality and fast time-to-tapeout. Furthermore, GUC provides a one-stop turnkey solution through its partnerships with world-class assembly and testing houses. Today, GUC has customers throughout Japan, Korea, China, North America and Europe. Its dedication in providing a one-stop, full-service SoC design foundry will benefit more customers from product concept to revenue shipment in the shortest time. In 2003, TSMC, the semiconductor foundry leader, became the primary shareholder. With the partnership of TSMC, GUC continues to excel to be the best SoC design foundry and solution provider. For more information, please visit http://www.globalunichip.com/ .

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SOURCE: Synopsys, Inc.

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