Synopsys Acquires Assets of LEDA Design

Experienced IP Engineers to Help Meet Demand for Synopsys' DesignWare Mixed-Signal IP

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MOUNTAIN VIEW, Calif.

Synopsys, Inc. (NASDAQ: SNPS), the world leader in semiconductor design software, today announced that it has acquired certain assets and hired the engineering team of LEDA Design, a developer of mixed-signal intellectual property (IP). LEDA Design has a team of more than 80 experienced digital and mixed-signal IP design engineers and support personnel located in Yerevan, Armenia who will join the Synopsys DesignWare® IP engineering team. These talented engineers have a track record of successfully developing and delivering IP and will help Synopsys meet increasing customer demand for Synopsys' portfolio of DesignWare IP. The LEDA Design team has worked together for more than four years developing, marketing and selling silicon-verified analog, digital and mixed-signal IP, as well as digital core and IO libraries. The terms of the transaction are not being disclosed.

"Synopsys is committed to providing its customers with the most comprehensive standards-based IP portfolio," said John Chilton, senior vice president and general manager of the Solutions Group at Synopsys. "The addition of the LEDA Design team in Armenia to our worldwide engineering team extends our expertise in IP and enables us to address increasing customer demand."

Synopsys DesignWare IP is the leading standards-based IP portfolio in the world. The DesignWare portfolio includes a complete family of digital and mixed-signal IP for widely used industry standard protocols, such as Universal Serial Bus (USB) and PCI Express™.

Forward-Looking Statements

This press release contains forward-looking statements within the meaning of the safe harbor provisions of Section 21E of the Securities Exchange Act of 1934, including statements regarding the expected benefits of the acquisition. These statements are based on Synopsys' current expectations and beliefs. Actual results could differ materially from the results implied by these statements as a result of unforeseen difficulties in integrating LEDA Design's engineering team into Synopsys' DesignWare® IP engineering team, as well as the factors described in the section of Synopsys' Quarterly Report on Form 10-Q for the fiscal quarter ended July 31, 2004 filed with the Securities and Exchange Commission entitled "Factors That May Affect Future Results."

About Synopsys

Synopsys, Inc. is the world leader in electronic design automation (EDA) software for semiconductor design. The company delivers technology-leading semiconductor design and verification platforms and IC manufacturing software products to the global electronics market, enabling the development and production of complex systems-on-chips (SoCs). Synopsys also provides intellectual property and design services to simplify the design process and accelerate time-to-market for its customers. Synopsys is headquartered in Mountain View, California and has offices in more than 60 locations throughout North America, Europe, Japan and Asia. Visit Synopsys online at http://www.synopsys.com/ .

NOTE: Synopsys and DesignWare are registered trademarks of Synopsys, Inc. All other trademarks or registered trademarks mentioned in this release are the intellectual property of their respective owners.

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