Synopsys Announces Analyst Meeting at the 41st Design Automation Conference (DAC)

Synopsys, Inc. (NASDAQ: SNPS), the world leader in semiconductor design software, announces that it is webcasting a meeting with analysts taking place at the 41st Design Automation Conference (DAC) in San Diego, California.

This event will be carried live on the Internet at the Synopsys corporate website at https://www.synopsys.com/corporate/invest/invest.html on Tuesday, June 8, 2004 at 8:00 a.m. PDT (11:00 a.m. EDT). To access the live audio webcast, please go to the website at least 15 minutes early to register and to download and install any necessary audio software. The webcast replay can be accessed at the Synopsys corporate website approximately 30 - 45 minutes after the conclusion of the live event.

About Synopsys

Synopsys, Inc. is the world leader in electronic design automation (EDA) software for semiconductor design. The company delivers technology-leading semiconductor design and verification platforms and IC manufacturing software products to the global electronics market, enabling the development and production of complex systems-on-chips (SoCs). Synopsys also provides intellectual property and design services to simplify the design process and accelerate time-to-market for its customers. Synopsys is headquartered in Mountain View, California and has offices in more than 60 locations throughout North America, Europe, Japan and Asia. Visit Synopsys online at https://www.synopsys.com/.

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SOURCE: Synopsys, Inc.

CONTACT: investors, Jessica Kourakos of Synopsys, Inc., +1-650-584-1901,
or jessicak@synopsys.com

Web site: https://www.synopsys.com/