Renesas Technology Selects Synopsys Star-RCXT for 90 Nanometer Designs

Star-RCXT Delivers Silicon-Accurate Parasitic Extraction for Advanced Technologies

Synopsys Inc. (NASDAQ: SNPS), the world leader in semiconductor design software, today announced that Renesas Technology Corp., a joint venture between Hitachi, Ltd. (NYSE: HIT)(TSE: 6501) and Mitsubishi Electric Corporation (TSE: 6503), has standardized on Synopsys' Star-RCXT™ for resistive-capacitive (RC) parasitic extraction in its design flow. With Synopsys' Star-RCXT in their Galaxy™ Design flow, Renesas Technology now has a complete sign-off solution that accelerates timing and signal integrity (SI) closure for their 130- and 90-nanometer (nm) designs.

"Using Synopsys' Star-RCXT for accurate RC parasitic extraction with PrimeTime® for static timing analysis, we successfully taped out several multimillion-gate chips," said Hisaharu Miwa, department manager, EDA Technology Development Department, Design Technology Division, LSI Product Technology Unit, Renesas Technology Corp. "Based on these results, we have standardized on Star-RCXT for parasitic extraction in our sign-off flow for 90 nanometer designs."

Star-RCXT is the market-leading parasitic extraction solution, offering the highest performance, capacity and accuracy for cell-based, custom digital and analog/mixed signal designs. Star-RCXT supports key 90nm process capabilities including in-die process variation solutions -- such as selective process biasing (spacing and width dependent metal bias), local density effects (LDE), length of diffusion (LOD) -- and metal fill. In modeling such advanced silicon process features, Star-RCXT extracts highly accurate RC parasitics for PrimeTime SI to ensure concurrent timing and signal integrity sign-off.

"Synopsys Star-RCXT and PrimeTime are the standard for silicon correlation and sign-off in eight out of the ten leading semiconductor design flows today," said Antun Domic, senior vice president and general manager of Synopsys' Implementation Group. "By standardizing on Synopsys' proven sign- off solution within their Galaxy Design flow, Renesas Technology is now able to deliver its next-generation products to market with first-pass silicon success."

About Synopsys

Synopsys, Inc. is the world leader in electronic design automation (EDA) software for semiconductor design. The company delivers technology-leading semiconductor design and verification platforms and IC manufacturing software products to the global electronics market, enabling the development and production of complex systems-on-chips (SoCs). Synopsys also provides intellectual property and design services to simplify the design process and accelerate time-to-market for its customers. Synopsys is headquartered in Mountain View, California, and has more than 60 offices located throughout North America, Europe, Japan and Asia. Visit Synopsys online at https://www.synopsys.com/ .

NOTE: Synopsys and PrimeTime are registered trademarks of Synopsys, Inc. Galaxy and Star-RCXT are trademarks of Synopsys. All other trademarks or registered trademarks mentioned in this release are the intellectual property of their respective owners.

SOURCE: Synopsys Inc.

CONTACT: editorial, Nancy Renzullo of Synopsys, Inc., +1-650-584-1669,
or renzullo@synopsys.com; or Andrea Zils of Edelman Public Relations,
+1-650-429-2731, or andrea.zils@edelman.com, for Synopsys Inc.

Web site: https://www.synopsys.com/