Synopsys Announces National Semiconductor Standardizes on Synopsys VCS, Leda, and Formality for Company-Wide Use

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Synopsys, Inc. (NASDAQ: SNPS), the world leader in semiconductor design software, today announced National Semiconductor Corporation has standardized on its' VCS® RTL verification solution, the cornerstone of the Discovery™ Verification Platform, Leda® programmable design checker, and Formality® functional equivalence checker. Advanced, built-in verification technologies in VCS, the pre-packaged rules and ease of use of Leda, and the performance and capacity delivered by Formality enable National Semiconductor's verification teams worldwide to reduce overall verification time while delivering higher quality products.

"VCS delivers the performance, capacity and built-in verification technologies we need to effectively verify our most complex designs," said James Lin, vice president of the Technology Infrastructure Group at National Semiconductor. "The comprehensive coverage capabilities built into VCS are widely used at National Semiconductor because they are fast, easy to use and improve verification quality. Additionally, the new built-in full-featured testbench and assertion technologies in VCS will further improve our verification productivity."

"After evaluation of various tools in the market place, we have standardized on Leda and Formality for their technology and performance advantages," said Dennis Lau, senior manager of the Design Tools Group, at National Semiconductor. "Leda addresses our need for a powerful, configurable RTL and netlist checker and Formality delivers the performance and capacity needed to perform equivalence checking on our large designs. Combined, VCS, Formality and Leda enable us to develop high-quality SoCs."

"National Semiconductor chose our products because they can raise the quality of National's verification environment to allow them to achieve first-pass silicon," said Farhad Hayat, vice president of marketing for the Verification Group at Synopsys. "By evolving VCS into a complete RTL verification solution, Synopsys is delivering a cost-effective solution with best-in-class technologies to meet the functional verification needs for complex designs from companies like National Semiconductor."

About Synopsys

Synopsys, Inc. is the world leader in electronic design automation (EDA) software for semiconductor design. The company delivers technology-leading semiconductor design and verification platforms and IC manufacturing software products to the global electronics market, enabling the development and production of complex systems-on-chips (SoCs). Synopsys also provides intellectual property and design services to simplify the design process and accelerate time-to-market for its customers. Synopsys is headquartered in Mountain View, California, and has more than 60 offices located throughout North America, Europe, Japan and Asia. Visit Synopsys online at http://www.synopsys.com/ .

NOTE: Synopsys, Leda, Formality, and VCS are registered trademarks of Synopsys, Inc, and Discovery is a trademark of Synopsys, Inc. All other trademarks or registered trademarks mentioned in this release are the intellectual property of their respective owners.

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