Renesas Design Vietnam Selects Synopsys as Its EDA Provider for SoC Design

Synopsys' Galaxy™ Design and Discovery™ Verification Platforms and DesignWare® IP to Accelerate SoC Development

PRNewswire-FirstCall
MOUNTAIN VIEW, Calif.

Synopsys, Inc. (NASDAQ: SNPS), a world leader in semiconductor design software, today announced that Renesas Design Vietnam Co. (RVC), Ltd, a system LSI semiconductor design subsidiary of the leading microcontroller supplier Renesas Technology Corp., has chosen Synopsys as its electronic design automation (EDA) provider for system-on-chip (SoC) design. RVC will use Synopsys' Galaxy™ Design and Discovery™ Verification Platforms, as well as Synopsys' DesignWare® Library intellectual property (IP), to speed development of SoCs for Renesas' SH-Mobile application processors used in mobile phones and consumer electronics products.

"We have used Synopsys solutions worldwide with great success for our design activities to develop and implement SoCs for mobile, automotive and PC/audio-visual applications," said Mr. Tsuneo Sato, president and CEO at Renesas Design Vietnam Co., Ltd. "Synopsys' complete RTL-to-GDSII flows and proven IP deliver the performance and productivity we need for our advanced SoC designs. Synopsys is an important partner for the design and implementation of SoCs for a wide range of products."

"Renesas Vietnam adopted Synopsys' solutions to address their design challenges because they require the highest-quality tools and IP for their SoCs," said Geok-Cheng Tan, director of Area Sales at Synopsys. "We support Vietnam's talented engineers and are committed to helping Vietnam grow as a technology leader."

"It's gratifying to have RVC, a design subsidiary of a world-class semiconductor manufacturer such as Renesas Technology, choose Synopsys as its EDA provider," said Howard Ko, vice president of Asia Pacific Sales at Synopsys. "We've enjoyed a close working relationship with Renesas Technology at many of its worldwide facilities and look forward to helping them develop SoCs for the burgeoning electronics market in Vietnam. We will continue to work with Renesas Technology to ensure predictable success for their SoC designs."

About Synopsys

Synopsys, Inc. is a world leader in EDA software for semiconductor design. The company delivers technology-leading semiconductor design and verification platforms and IC manufacturing software products to the global electronics market, enabling the development and production of complex systems-on-chips (SoCs). Synopsys also provides intellectual property and design services to simplify the design process and accelerate time-to-market for its customers. Synopsys is headquartered in Mountain View, California and has offices in more than 60 locations throughout North America, Europe, Japan and Asia. Visit Synopsys online at http://www.synopsys.com/.

NOTE: Synopsys and DesignWare are registered trademarks of Synopsys, Inc. Galaxy and Discovery are trademarks of Synopsys. Any other trademarks or registered trademarks mentioned in this release are the intellectual property of their respective owners.

Editorial Contacts:

Sheryl Gulizia
Synopsys, Inc.
650-584-8635
sgulizia@synopsys.com

Jesse Ho
Synopsys, Inc.
886 2 3725 5826
jesseho@synopsys.com

SOURCE: Synopsys, Inc.

CONTACT: Sheryl Gulizia, +1-650-584-8635, or sgulizia@synopsys.com, or
Jesse Ho, 886 2 3725 5826, or jesseho@synopsys.com, both of Synopsys, Inc.

Web site: http://www.synopsys.com/