Displaytech Switches to Synopsys Design Compiler Tool for Next-Generation FLCOS Microdisplays

Predictable Performance and Excellent Support Essential for Development of High-Performance Microdisplays

Synopsys, Inc. (NASDAQ: SNPS), a world leader in semiconductor design software, today announced that Displaytech Inc., a world leader in ferroelectric liquid-crystal-on-silicon (FLCOS) microdisplays, has moved to the Synopsys Design Compiler® synthesis tool in designing its next-generation FLCOS backplane for high-resolution, high-speed microprojection and holographic data storage (HDS) systems. These new microdisplays deliver high-speed light switching to produce brilliant, real-life digital images. The advanced timing and power synthesis capabilities in the Design Compiler tool proved crucial in meeting Displaytech's design goals within schedule.

"We needed a new synthesis solution to deal with the challenging timing and power goals for our next-generation microdisplays and found Design Compiler to be a superior synthesis solution capable of addressing our design challenges," said Bruce Spenner, executive vice president at Displaytech Inc., in Longmont Colorado. "In addition, Synopsys demonstrated a real commitment to our success through the excellent support we received throughout the design process, thereby helping us meet our tapeout deadlines."

Significant enhancements have been implemented in the Design Compiler solution over the last year. With the introduction of topographical technology, which utilizes Synopsys' advanced physical implementation technologies to drive accurate timing, designers can now accurately predict post-layout timing, area, and power during synthesis. As a result, RTL designers can eliminate costly iterations between synthesis and layout and generate a better start point for physical implementation faster. This, combined with the timing, area, advanced low-power, and runtime improvements in the Design Compiler 2006 release, significantly increases designer productivity and address increasingly complex design challenges.

"We continue to innovate in synthesis to enable our customers to meet their most demanding design challenges while shortening their overall chip development schedules," said Antun Domic, senior vice president and general manager of Synopsys Implementation Group. "Customers like Displaytech depend on the performance and productivity that Design Compiler delivers to meet their critical time-to-market goals."

About Synopsys

Synopsys, Inc. is a world leader in electronic design automation (EDA) software for semiconductor design. The company delivers technology-leading semiconductor design and verification platforms and IC manufacturing software products to the global electronics market, enabling the development and production of complex systems-on-chips (SoCs). Synopsys also provides intellectual property and design services to simplify the design process and accelerate time-to-market for its customers. Synopsys is headquartered in Mountain View, California, and has more than 60 offices located throughout North America, Europe, Japan and Asia. Visit Synopsys online at www.synopsys.com .

About Displaytech

Displaytech, Inc. dramatically improves display-based products by providing high-switching-speed FLCOS as microdisplays that deliver brilliant, real-life, digital images enabling next-generation consumer electronics and storage products by providing FLCOS as a Spatial Light Modulator (SLM). The company has over 100 granted, licensed and pending patents and has received numerous honors and awards including being ranked as one of the fastest growing technology companies in North America by Deloitte and Touche in 2005. Visit Displaytech online at www.displaytech.com .

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