ADVISORY/ Synopsys to Highlight Complete Wireless USB, PCI Express, SATA and Mobile Storage IP Solutions at Intel Developers' Forum 2006

PRNewswire-FirstCall
MOUNTAIN VIEW, Calif.

Synopsys, Inc. (NASDAQ: SNPS), a world leader in semiconductor design software, will highlight its complete wireless USB, PCI Express®, SATA and Mobile Storage IP solutions at the Intel Developers' Forum in San Francisco, California on March 7, 2006. Synopsys is the leader in connectivity IP offering a complete solution of digital, PHY and verification IP. The Intel Developer Forum (IDF) is one of the premier technical events for the release and exchange of new technology and information in the computer, network, wireless and communications realms.

WHAT: Visit Synopsys at IDF and see how the world leader in
        standards-based connectivity IP can help with your design
        challenges. We will be showcasing DesignWare® IP solutions in
        the PCI Express and Wireless USB communities. In addition, the
        DesignWare SATA IP solution will be demonstrated in the SATA I/O
        booth #135, and the Mobile Storage IP solution will be presented
        at the Finisar booth # 530.

WHEN: March 7-9, 2006

WHERE: Moscone Center West, 800 Howard Street San Francisco, CA 94103

IDF EXHIBIT HOURS:
Tuesday, March 7, 5:00 pm - 8:00 pm
Wednesday, March 8, 12:00 pm - 2:00 pm & 6:00 pm - 8:00 pm
Thursday, March 9, 10:00 am - 12:00 pm

SYNOPSYS HIGHLIGHTS AT IDF:

-- Synopsys Booth #114 -- Complete PCI Express IP Solution
  Synopsys delivers a complete PCI Express IP solution consisting of a
  suite of digital controllers, PHY IP and verification IP. [Silicon-
  proven and shipping in volume production by leading technology
  companies worldwide, DesignWare IP is a trusted, cost effective and low
  risk choice for chip success. Come talk to us about the latest
  improvements in PCI Express IP, including our support for the PCI
  Express 2.0 (or Gen II) specifications. See how DesignWare IP for PCI
  Express is embedded into our customer products, which are shipping in
  volume production. For more information on the complete DesignWare IP
  solution for PCI Express, please visit:
  http://www.synopsys.com/pciexpress .

-- Synopsys Booth #810 -- Wireless USB IP Solution
  Come see the industry's first Wireless USB (WiUSB) Device IP, based on
  the Certified Wireless USB specification from the USB-IF. The
  DesignWare WiUSB Device Controller enables a wide range of portable
  electronic or PC peripherals to connect without the need for cables.
  The demonstration will show real-time wireless USB data transfer from a
  device to a Windows PC using Synopsys DesignWare WiUSB IP. For more
  information on the DesignWare Wireless USB IP solution, please visit:
  https://www.synopsys.com/designware-ip/interface-ip/usb.html .

-- Finisar Booth #530 -- Synopsys' Mobile Storage IP Solution: Featuring
  CE-ATA
  Mobile storage addresses the growing storage need in battery-powered
  products requiring low power. The Synopsys DesignWare Mobile Storage IP
  core is a configurable host controller that supports the SD, MMC and
  CE-ATA mobile storage protocols. Come see the DesignWare Mobile Storage
  Host Controller interoperate with the Hitachi 3K8 CE-ATA drive. We will
  also show how the traffic can easily be viewed and debugged using
  Finisar Corporation's (NASDAQ: FNSR) latest new Bus Doctor™ CE-ATA
  Analyzer. For more information on the DesignWare Mobile Storage IP
  solution, please visit:

https://www.synopsys.com/designware-ip/interface-ip/mobile-storage.html

-- SATA-IO Booth #135 - Synopsys' Complete SATA IP Solution
Serial Advanced Technology Attachment (SATA), the evolutionary
replacement for the parallel ATA interface, is designed to handle
high-speed storage in desktops, mobile computing and network storage
applications. Come see the complete, silicon-proven DesignWare SATA IP
solution, consisting of the host controller, PHY IP and verification IP
running at 3Gbps with Native Command Queuing (NCQ). For more
information on the complete DesignWare SATA IP solution, please visit:
https://www.synopsys.com/designware-ip/interface-ip/sata.html .

For more information on IDF, please visit: www.intel.com/idf.

About Synopsys

Synopsys, Inc. is a world leader in electronic design automation (EDA) software for semiconductor design. The company delivers technology-leading semiconductor design and verification platforms and IC manufacturing software products to the global electronics market, enabling the development and production of complex systems-on-chips (SoCs). Synopsys also provides intellectual property and design services to simplify the design process and accelerate time-to-market for its customers. Synopsys is headquartered in Mountain View, California and has offices in more than 60 locations throughout North America, Europe, Japan and Asia. Visit Synopsys online at http://www.synopsys.com/ .

NOTE: Synopsys and DesignWare are registered trademarks of Synopsys, Inc. All other trademarks or registered trademarks mentioned in this release are the intellectual property of their respective owners.

Press Contact:
Renae Veiga
Synopsys, Inc.
650-584-1902
rveiga@synopsys.com

SOURCE: Synopsys, Inc.

CONTACT: Renae Veiga of Synopsys, Inc., +1-650-584-1902, or
rveiga@synopsys.com

Web site: http://www.synopsys.com/
http://www.intel.com/idf