Synopsys Galaxy Platform Reduces Power Consumption of Industry-Leading Multi- Voltage Designs

Multiple Multi-Voltage Tapeouts Completed With Galaxy Advanced Low-Power Solution

Synopsys, Inc. (NASDAQ: SNPS), a world leader in semiconductor design software, today announced that ARM, DSP Group Inc., Freescale, Infineon Technologies AG, Matsushita Electric Industrial Co., Ltd., Realtek Semiconductor Corp., Samsung, STMicroelectronics, Toshiba, UMC and others have successfully taped out more than 15 multi-voltage designs with Synopsys' Galaxy™ design platform RTL-to-GDSII advanced low-power solution. These leading designs target wireless, media processor, medical, DSP, MPEG4, mobile TV, and other applications. Synopsys' Galaxy design platform provides a complete RTL-to- GDSII low-power solution that has enabled customers to develop more competitive products and get them to market faster.

The Galaxy design platform delivers the lowest power consumption and highest productivity through its complete low-power solution. It includes the most advanced low-power techniques, such as multi-voltage and MTCMOS power gating, as well as more commonly used techniques such as clock gating and multi-threshold voltages. In addition, it performs comprehensive dynamic and leakage power optimizations and analyses throughout the synthesis, physical design, and sign-off phases of the design process.

The Galaxy low-power flow is complemented by the complete set of standards required for successful low-power design. These include the open-source Liberty™ library modeling technology which includes the new Composite Current Source (CCS) power extension that provides the accuracy and scaling required for power modeling at sub-90 nanometer geometries, the open-source Synopsys Design Constraints (SDC) format for design and low-power constraints including the new power domain definitions that help specify low-power intent early in the design process, and the open-source Switching Activity Interchange Format (SAIF) for switching activity interchange between tools in the flow.

"Our customers are deploying our most advanced low-power techniques to increase their market competitiveness," said Antun Domic, senior vice president and general manager of the Implementation Group at Synopsys. "The Galaxy low-power solution has been proven through multiple customer tapeouts to deliver superior results and accelerated time-to-market on some of today's most challenging low-power designs."

About Synopsys

Synopsys, Inc. is a world leader in electronic design automation (EDA) software for semiconductor design. The company delivers technology-leading semiconductor design and verification platforms and IC manufacturing software products to the global electronics market, enabling the development and production of complex systems-on-chips (SoCs). Synopsys also provides intellectual property and design services to simplify the design process and accelerate time-to-market for its customers. Synopsys is headquartered in Mountain View, California and has offices in more than 60 locations throughout North America, Europe, Japan and Asia. Visit Synopsys online at https://www.synopsys.com/.

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