ARM Recipient of Synopsys Fifth Annual Tenzing Norgay Interoperability Achievement Award

Collaboration on Interoperable Design Solutions to Benefit User Community

Synopsys, Inc. (NASDAQ: SNPS), a world leader in semiconductor design software and interoperability, today presented to ARM, the fifth annual Tenzing Norgay electronic design automation (EDA) Interoperability Achievement Award. The award recognizes ARM's collaborative EDA contributions, which include long-term support of the Liberty™ standard format; its standard proposals to the Structure for Packaging, Integrating and Re-using IP within Tool-flows (SPIRIT) Consortium; its efforts in establishing a reference verification methodology based on SystemVerilog; and its drive to establish reference methodologies (RM) for implementing ARM® microprocessors, including ARM Intelligent Energy Manager (IEM) technology, in silicon. ARM was formally recognized today at the Synopsys Interoperability Breakfast event at the 42nd Design Automation Conference (DAC).

"We are honored to accept the Tenzing Norgay Interoperability Achievement Award in recognition of our ongoing contributions," said Neal Carney, VP of Marketing, Physical IP, ARM. "The goal of our collaborations is to make ARM IP easy to use in every chip design and to support/create industry standards, to promote advanced solutions and to reduce the cost of system-on-chip design. Through our participation in supporting established and emerging standards like Liberty, SPIRIT, and SystemVerilog, and through our efforts in establishing reference methodologies including the RM extensions for IEM low-power technology, we help make available to our customers cost-effective, integrated, yet highly flexible IC design options."

ARM is committed to interoperability and continually shows its dedication by supporting the widely used Liberty format, including advanced modeling. The ARM co-authored SystemVerilog Verification Methodology Manual, describes advanced verification techniques using SystemVerilog. As a key member of the SPIRIT Consortium, ARM has developed standards for IP descriptions as well as tools that increase automation levels and cut costs while improving ease-of-use and increasing flexibility in IP selection and integration. ARM also jointly developed with Synopsys a low-power RM for implementing ARM IEM technology-enabled processors, validated with the ARM Artisan® Metro™ low-power multi-voltage-enabled library, that can reduce ARM processor energy consumption by up to 60 percent.

"Interoperability plays a key role in elevating designers to a higher level of productivity, just as Tenzing Norgay's efforts facilitated the first ascent to the top of Mount Everest," said Rich Goldman, vice president, Strategic Alliance at Synopsys. "We recognize that successful EDA interoperability is a result of contributions and collaboration such as ARM has demonstrated. Their standard-compliant IP and Liberty format interoperable solutions complement the efforts from Synopsys and other companies who continue to push for even greater magnitudes of interoperability."

About the Tenzing Norgay Interoperability Achievement Award

The Tenzing Norgay EDA Interoperability Achievement Award was established by Synopsys to recognize EDA providers who collaborate on interoperable design flows that benefit the user community. The award is presented annually to an EDA company that has surpassed common levels of interoperability, has contributed to overall industry advancement, and has helped provide a new view of the future for EDA interoperability. Named for the crucial role that Sherpa Tenzing Norgay played in the first successful attempt to reach the summit of Mount Everest in 1953, the EDA Interoperability Achievement Award recognizes achievements that are critical to a designer's success.

About Synopsys

Synopsys, Inc. is a world leader in EDA software for semiconductor design. The company delivers technology-leading semiconductor design and verification platforms and IC manufacturing software products to the global electronics market, enabling the development and production of complex systems-on-chips (SoCs). Synopsys also provides intellectual property and design services to simplify the design process and accelerate time-to-market for its customers. Synopsys is headquartered in Mountain View, California and has offices in more than 60 locations throughout North America, Europe, Japan and Asia. Visit Synopsys online at http:/www.synopsys.com .

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