Synopsys Leads Industry With New Nanometer Modeling Technology

Composite Current Source Model First to Combine Timing, Noise and Power

Synopsys, Inc. (SNPS), a world leader in semiconductor design software, today announced the availability of the industry's first unified current source model for accurate and efficient modeling of nanometer effects. Using Synopsys' Composite Current Source (CCS) modeling technology, designers are, for the first time, able to perform comprehensive timing, noise and power analysis using a single, open library model. Anchored by the industry's golden sign-off tool, Synopsys' PrimeTime® tool, CCS modeling technology extends sign-off to include noise and power effects that are required for today's nanometer (nm) designs.

Accurate physical IP models are essential for implementation and sign-off tools. With the move to 90-nm and below, many new effects in timing, noise and power need to be accurately modeled. Some of these effects are inter-dependent and can no longer be analyzed in isolation. CCS modeling technology allows designers to reduce design margin and deliver designs with higher performance, less area and lower power consumption. To speed adoption of CCS, Synopsys is providing a complete support system for library creation and validation including: open source Liberty™ modeling format, parsers, characterization/validation tools and guidelines.

"The ARM and Synopsys collaboration is focused on advancing design solutions based on open standards," said Neal Carney, vice president of Marketing, Physical IP, ARM. "CCS is an innovative, unified model that addresses the industry's advanced process demands and new design styles. In addition to advanced Liberty models currently available for ARM® Physical IP, we will be starting delivery of production CCS timing models for our Advantage™ and Metro™ 90-nanometer standard cell libraries in Q4 of 2005."

"For more than 15 years, Synopsys has led the industry in library modeling technology as well as in programs and initiatives for open source standards," said Antun Domic, senior vice president and general manager, Synopsys Implementation Group. "Our latest innovations in CCS modeling technology provide a comprehensive solution to address the immense modeling challenges of nanometer designs. Our collaboration with industry-leading IP providers such as ARM will ensure that our mutual customers get access to the industry's most advanced design tools and libraries."

About Synopsys

Synopsys, Inc. is a world leader in EDA software for semiconductor design. The company delivers technology-leading semiconductor design and verification platforms and IC manufacturing software products to the global electronics market, enabling the development and production of complex systems-on-chips (SoCs). Synopsys also provides intellectual property and design services to simplify the design process and accelerate time-to-market for its customers. Synopsys is headquartered in Mountain View, California and has offices in more than 60 locations throughout North America, Europe, Japan and Asia. Visit Synopsys online at http:/www.synopsys.com /.

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