STARC Standardizes on Synopsys Innovations for 90-Nanometer Low Power Design in STARCAD-21 Flow

Galaxy Platform Enables 50 Percent Power Savings in SoC Design

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Synopsys, Inc. (NASDAQ: SNPS), a world leader in semiconductor design software, today announced the integration of the Synopsys Galaxy™ Design Platform into the latest Semiconductor Technology Academic Research Center (STARC) STARCAD-21 Synopsys-based production flow Version 2.0, offering a complete RTL-to-GDSII solution for low power design. This latest STARC production flow includes key Synopsys technologies for ultra low power design. Proving out the flow through a 90-nanometer (nm) test system-on-chip (SoC), STARC demonstrated a 50 percent power savings utilizing advanced low power design techniques enabled by the Galaxy-based production flow. Given these results, STARC and Synopsys customers are now adopting the flow for their next-generation low power designs.

"Our customers demand a world-class low power design environment, which is why we have continued to partner with Synopsys in the development of our STARCAD-21 Synopsys-based production flow Version 2.0," said Nobuyuki Nishiguchi, senior manager, design methodology group, STARC. "The Synopsys Galaxy Design Platform offers a highly advanced, low-power portfolio in a fully automated flow delivering the best combination of lowest power and highest performance for our design. We are impressed by the fast convergence and consistent quality of results that Galaxy provides for the most demanding low-power designs."

Designers using the STARCAD-21 production flow will have access to Galaxy Design Platform's complete portfolio of application-driven low-power techniques targeted to the wireless, mobile, home electronics, computing and networking markets. The STARCAD-21 flow is specifically developed around Galaxy's complete RTL-to-GDSII platform to significantly reduce power while maximizing the quality of results for timing, area, and yield. New in STARCAD-21 are key Galaxy innovations to support multi-voltage techniques to manage dynamic power as well as aggressive multi-threshold optimizations and power gating (deep-sleep, shutdown) techniques to manage standby leakage power. These advanced low-power techniques are fully automated throughout the flow-from RTL synthesis, design planning, physical synthesis, clock tree synthesis and routing to sign-off.

"We are pleased to see the rapid adoption of Galaxy-enabled STARCAD-21 Synopsys-based production flow Version 2.0 in several of our member companies," continued Nishiguchi. "By basing this production flow entirely on Galaxy platform, we are able to ensure our customers have access to a proven and automated RTL-to-GDSII design environment for their advanced low power applications."

"Synopsys and STARC have worked closely to deliver a complete low-power RTL-to-GDSII production flow which is of extreme importance for top semiconductor companies in Japan," said Antun Domic, senior vice president and general manager of Synopsys' Implementation Group. "With the STARCAD-21 production flow, customers now have access to the most advanced low-power techniques in the Galaxy Design Platform to build competitive power-sensitive devices with fast production times to meet rising consumer demand."

About STARC's STARCAD-21 Synopsys-based Production Flow Version 2.0

The Galaxy Design Platform for RTL Synthesis, Physical Design and Sign-off is a key component of STARC's STARCAD-21 production flow. Synopsys' Discovery™ Platform for Design verification and DesignWare® Library, the most widely used silicon intellectual property (IP), are also an integral part of the production flow. The Synopsys Professional Services group has expertise with the STARC production flow and can provide deployment assistance and design support to users of the STARCAD-21 flow.

About Synopsys

Synopsys, Inc. is a world leader in electronic design automation (EDA) software for semiconductor design. The company delivers technology-leading semiconductor design and verification platforms and IC manufacturing software products to the global electronics market, enabling the development and production of complex systems-on-chips (SoCs). Synopsys also provides intellectual property and design services to simplify the design process and accelerate time-to-market for its customers. Synopsys is headquartered in Mountain View, California, and has more than 60 offices located throughout North America, Europe, Japan and Asia. Visit Synopsys online at http://www.synopsys.com/ .

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Synopsys Editorial Contacts:
Nancy Renzullo
Synopsys, Inc.
650-584-1669
renzullo@synopsys.com

Sarah Seifert
Edelman Public Relations
650-429-2776
Sarah.seifert@edelman.com

SOURCE: Synopsys, Inc.

CONTACT: Nancy Renzullo of Synopsys, Inc., +1-650-584-1669, or renzullo@synopsys.com; or Sarah Seifert of Edelman Public Relations, +1-650-429-2776, or Sarah.seifert@edelman.com, for Synopsys, Inc.

Web site: http://www.synopsys.com/