Marvell Adopts Synopsys' Galaxy Platform for High-Performance Communications Products

Communications Leader Uses Synopsys' Tools and Flows From Design Through Physical Implementation

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Synopsys, Inc. (NASDAQ: SNPS), a world leader in semiconductor design software, today announced that Marvell, a leader in high-performance broadband communications technologies, has adopted Synopsys' Galaxy™ Design Platform as a RTL-to-GDSII design solution to develop networking and storage products. Marvell uses Synopsys' Galaxy platform to help develop complex, high-performance devices that touch every point of the communications infrastructure such as switching, transceivers, wireless, PC connectivity, communications controllers and storage.

"Marvell has been a longtime user of Synopsys' design and verification technology and uses key tools such as Design Compiler®, Astro™, PrimeTime® and VCS® to get our high-performance products to market," said Pantas Sutardja, chief technical officer at Marvell. "We use Design Compiler and Astro as our primary synthesis and place-and-route solution. Adding Synopsys' Astro-Rail™, DFT MAX, JupiterXT™ and Physical Compiler® technology has further strengthened our flow and improved our time to results."

Marvell's design implementation suite includes Synopsys' Galaxy products including Design Compiler RTL synthesis solution, JupiterXT physical planning solution, Physical Compiler and Astro physical implementation solution, DFT MAX 1-pass test synthesis solution, Power Compiler™ power management solution, Astro-Rail power rail analysis solution, Astro-Xtalk™ cross-talk analysis solution, PrimeTime static timing sign-off solution, Star-RCXT™ parasitic extraction solution, and TetraMAX® automatic test generation (ATPG) solution. Additionally, Marvell uses Synopsys', VCS RTL verification solution and Vera® testbench automation.

"To see an industry leader like Marvell using our solutions for their high-performance, complex system-on-chip and networking products is truly gratifying," said Antun Domic, senior vice president and general manager of the Implementation Group at Synopsys. "Marvell and Synopsys have enjoyed a strong history of collaboration. In fact, Marvell was a key partner in the development of our power network synthesis technology -- part of our JupiterXT solution. We look forward to continued mutual success."

About Synopsys

Synopsys, Inc. is a world leader in EDA software for semiconductor design. The company delivers technology-leading semiconductor design and verification platforms and IC manufacturing software products to the global electronics market, enabling the development and production of complex systems-on-chips (SoCs). Synopsys also provides intellectual property and design services to simplify the design process and accelerate time-to-market for its customers. Synopsys is headquartered in Mountain View, California and has offices in more than 60 locations throughout North America, Europe, Japan and Asia. Visit Synopsys online at http:/www.synopsys.com.

NOTE: Synopsys, Design Compiler, Physical Compiler, PrimeTime, TetraMAX, VCS and Vera are registered trademarks of Synopsys, Inc. Astro, Astro-Xtalk, Astro-Rail, Galaxy, JupiterXT, Power Compiler, and Star-RCXT are trademarks of Synopsys. Any other trademarks or registered trademarks mentioned in this release are the intellectual property of their respective owners.

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