Design Compiler Topographical Technology-Based ARM-Synopsys Reference Methodology Delivers Higher Productivity

Enhanced Methodology Accelerates Time-to-Market for ARM Processors, Including Next-generation ARM Cortex-R4 Processor

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Synopsys, Inc. (NASDAQ: SNPS), a world leader in semiconductor design software, today announced the availability of the ARM-Synopsys Galaxy™ Reference Methodology (RM) with support for Synopsys' Design Compiler® topographical technology. Design Compiler's topographical technology accurately predicts post-layout timing and area in synthesis, helping eliminate costly iterations between synthesis and layout and enabling faster time-to-market. With the enhanced ARM-Synopsys RM, based on Synopsys' Galaxy Design Platform, ARM® Partners can achieve higher designer productivity and reduce overall design time for the wide range of synthesizable ARM processors, including the new Cortex™-R4 processor targeted at the embedded market.

"ARM Partners are continuously seeking ways to get to volume production faster. Utilizing Synopsys' topographical technology helps designers achieve a useful boost in productivity," said Keith Clarke, vice president of technical marketing at ARM. "The results generated by Design Compiler topographical technology have consistently correlated within five percent of post-layout timing and area on multiple processor cores, including the Cortex-R4 processor. Since Design Compiler topographical technology is included in the Galaxy Design Platform, it was very easy to integrate into our ARM-Synopsys Galaxy Reference Methodology."

Topographical technology gives RTL designers early visibility into post-layout design issues and allows them to address them while still in synthesis. It utilizes Synopsys' advanced physical implementation technologies to drive accurate timing and area prediction within the synthesis engine. As a result, RTL designers can eliminate costly iterations between synthesis and layout and generate a better start point for physical design to significantly reduce design time.

The ARM-Synopsys Galaxy RM, co-developed by ARM and Synopsys Professional Services and validated using ARM Artisan® Physical IP, offers methodology, documentation on best practices, and optimized scripts that leverage Synopsys' topographical technology, as well as many other new enhancements in the Galaxy 2005.09 release. Using the RM "out-of-the-box," ARM Partners can quickly and easily configure, implement, and verify synthesizable ARM processors to their chosen process technologies. In addition, with the ARM-Synopsys Galaxy RM, ARM Partners can produce highly accurate processor models for SoC integration.

"In today's competitive environment, designers are constantly pushing to reduce time-to-market," noted Antun Domic, senior vice president and general manager of Synopsys' Implementation Group. "ARM has incorporated support for Design Compiler topographical technology in its latest Galaxy platform-based ARM-Synopsys Reference Methodology to enable our mutual customers to accelerate time-to-market through a highly predictable, convergent path from RTL to GDSII."

Availability

The new ARM-Synopsys Galaxy RM incorporating Design Compiler topographical technology is available starting this month for Cortex-R4 Partners. Topographical technology-based RMs for the ARM926EJ-S™ processor, the ARM1176JZ(F)-S™ processor, and the Cortex-M3 processor is expected to be available in June, with additional processor support expected during the second half of 2006.

About Synopsys

Synopsys, Inc. is a world leader in electronic design automation (EDA) software for semiconductor design. The company delivers technology-leading semiconductor design and verification platforms and IC manufacturing software products to the global electronics market, enabling the development and production of complex systems-on-chips (SoCs). Synopsys also provides intellectual property and design services to simplify the design process and accelerate time-to-market for its customers. Synopsys is headquartered in Mountain View, California and has offices in more than 60 locations throughout North America, Europe, Japan and Asia. Visit Synopsys online at www.synopsys.com.

Forward-Looking Statements

This press release contains forward-looking statements within the meaning of Section 27A of the Securities Act of 1933 and Section 21E of the Securities Exchange Act of 1934, including expectations of the timing of additional processor support for the

ARM-Synopsys Reference Methodology (RM) using Synopsys' Design Compiler Topographical technology. These statements are based on current expectations and beliefs. Actual results could differ materially from these statements as a result of unforeseen difficulties in adapting the reference methodology to different processor platforms, uncertainties attendant to any new design flow or methodology and certain statements contained in the section of Synopsys' Quarterly Report on Form 10-Q for the fiscal quarter ended January 31, 2006 entitled "Management's Discussion and Analysis of Financial Condition and Results of Operations -- Factors That May Affect Future Results."

NOTE: Synopsys and Design Compiler are registered trademarks of Synopsys, Inc. Galaxy is a trademark of Synopsys, Inc.

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