Agere Systems Standardizes on Synopsys Platforms for Design and Verification of Digital Systems

Agere Chooses Synopsys in Major Productivity Drive

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Synopsys, Inc. (NASDAQ: SNPS), a world leader in semiconductor design software, and Agere Systems (NYSE: AGR), a global leader in semiconductors for storage, wireless data and public and enterprise networks, today announced that Agere has standardized on Synopsys' Galaxy™ Design and Discovery™ Platforms for implementation and verification in its digital design flows, including advanced 65-nanometer (nm) and below processes. Agere chose Synopsys as its primary supplier because of Synopsys' technology strength and ability to help Agere meet its complex system-on-chip (SoC) design requirements while improving quality, shortening time to results and increasing return on investment throughout the design and verification cycles. The agreement with Synopsys is part of Agere's plan to reduce redundancies and increase efficiency in its use of electronic design automation (EDA) software.

Agere and Synopsys will also work closely together to increase Agere's use of design for manufacturing (DFM) technology, which is being integrated with the Galaxy and Discovery platforms. Consolidation of Agere's digital EDA solutions will help the company streamline its design and verification cycles as it develops products that reduce SoC size, power consumption and costs for its customers.

"Over the years, we've worked closely with Synopsys to design our complex SoCs for a wide variety of markets," said Hai Wang, vice president Front End Operations, at Agere Systems. "We chose Synopsys to play a bigger role in our design and verification efforts going forward in light of Synopsys' outstanding solutions, service and support. Furthermore, Synopsys has a proven track record with us and with the foundries we use for silicon implementation. Using Synopsys as a design partner will help us consolidate our digital EDA solutions, which in turn will reduce costs and speed time to results."

"It's gratifying that leading-edge customers such as Agere choose Synopsys to be a major EDA partner in developing their advanced, ultra-deep submicron designs," said Chi-Foon Chan, president and chief operating officer at Synopsys. "Together, Synopsys and Agere can work to improve productivity and deliver more predictable results at every stage of the design, verification and implementation process."

About Synopsys

Synopsys, Inc. is a world leader in EDA software for semiconductor design. The company delivers technology-leading semiconductor design and verification platforms and IC manufacturing software products to the global electronics market, enabling the development and production of complex systems-on-chips (SoCs). Synopsys also provides intellectual property and design services to simplify the design process and accelerate time-to-market for its customers. Synopsys is headquartered in Mountain View, California and has offices in more than 60 locations throughout North America, Europe, Japan and Asia. Visit Synopsys online at http://www.synopsys.com/.

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