Synopsys Wins analogZONE'S 'Best Connectivity IP' Award for Its PCI Express, SATA and XAUI PHYs

analogZONE Recognizes High Performance and Low Power Capabilities for an Optimized, Silicon-proven Solution

Synopsys, Inc. (NASDAQ: SNPS), a world leader in semiconductor design software, today announced that analogZONE, a premier information source for electronic design engineers, has chosen Synopsys' DesignWare® SERDES-based PHYs for PCI Express®, SATA and XAUI as the best connectivity intellectual property (IP). analogZONE announced the selection in its Products of the Year 2006 issue published in February of this year.

"Synopsys has taken a fresh look at SERDES technologies and delivered a high-performance core that consumes half the power of a traditional design," said analogZONE's section editor Lee Goldberg in the award announcement. "This dedication, combined with Synopsys' rich self-diagnostic features, makes Synopsys' PHYs an ideal choice for PCIe, SATA or XAUI interfaces."

Synopsys' mixed-signal SERDES IP is part of Synopsys' complete portfolio of DesignWare cores, digital controllers and verification IP, which fully support the PCIe, XAUI and SATA standards. These high performance, mixed-signal PHYs offer highly differentiated, advanced built-in diagnostics for evaluating link performance and margin. The PHYs are designed using a robust architecture that tolerates process, voltage and temperature variations without compromising performance. Synopsys' silicon-proven IP provides an optimized, lower-risk, single vendor solution, and enables low power and reduced area design in systems-on-chips (SoCs) that use these high-performance standards.

"We are honored to receive this award from analogZONE for our High Performance SERDES-based IP" said John Koeter, group director for IP Marketing at Synopsys. "The innovative design significantly reduces chip power and test time while our complete solution -- which includes the digital, physical and verification IP for the PCIe, SATA and XAUI protocols -- provides faster time to market."

About DesignWare Cores

Synopsys DesignWare Cores provide system designers with silicon-proven, digital, and mixed-signal connectivity IP for some of the world's most recognized products, including communications processors, routers, switches, game consoles, digital cameras, computers and computer peripherals. Provided as synthesizable RTL source code or in GDS format, these cores enable designers to create innovative, cost-effective system-on-chips and embedded systems. Synopsys provides flexible licensing options for the DesignWare Cores. Each core can be licensed individually, on a fee-per-project basis, or users can opt for the Volume Purchase Agreement, which enables them to license all the cores as part of one simple agreement. For more information on DesignWare IP, visit: http://www.designware.com/ .

About Synopsys

Synopsys, Inc. is a world leader in EDA software for semiconductor design. The company delivers technology-leading semiconductor design and verification platforms and IC manufacturing software products to the global electronics market, enabling the development and production of complex systems-on-chips (SoCs). Synopsys also provides intellectual property and design services to simplify the design process and accelerate time-to-market for its customers. Synopsys is headquartered in Mountain View, California and has offices in more than 60 locations throughout North America, Europe, Japan and Asia. Visit Synopsys online at https://www.synopsys.com/ .

NOTE: Synopsys and DesignWare are registered trademarks of Synopsys, Inc. PCI Express is a registered trademark of PCI-SIG. Any other trademarks or registered trademarks mentioned in this release are the intellectual property of their respective owners.

Editorial Contact:

Yvette Huygen
Synopsys, Inc.
650-584-4547
yvetteh@synopsys.com

Ellen Van Etten
MCA
(970) 778-6094
evanetten@mcapr.com,

SOURCE: Synopsys, Inc.

CONTACT: Yvette Huygen of Synopsys, Inc., +1-650-584-4547, or yvetteh@synopsys.com; or Ellen Van Etten of MCA, +1-970-778-6094, or evanetten@mcapr.com, for Synopsys, Inc.

Web site: https://www.synopsys.com/