BAE Systems Uses Synopsys' Galaxy Design Platform to Achieve Success on 24 Radiation-Hardened Space-Qualified ASICs

Reliable Timing Closure, Tight Tool Integration Key to Complex Designs' Success

Synopsys, Inc. (NASDAQ: SNPS), the world leader in semiconductor design software, today announced that BAE Systems, an international company engaged in the development, delivery and support of advanced defense and aerospace systems, used Synopsys' Galaxy™ Design Platform to achieve success on 24 radiation-hardened space-qualified ASICs. Galaxy's predictive timing closure, as well as its tightly integrated flow, was instrumental in the design success of the complex chips.

At the heart of the Galaxy flow is Synopsys' Milkyway™ database, which allows multiple design teams to work together on designs via a common, seamless data interface. During a difficult development cycle on some of the chips, the Milkyway database enabled multiple design teams to work together on 12 of the ASICs simultaneously. The chips are all full military-aerospace quality, certified for use on multiple DOD (U.S. Department of Defense) and commercial satellite programs.

"Our design teams' expertise, coupled with the seamless tool integration in the Galaxy flow, was paramount in meeting tight deadlines and achieving success," said David Oates, space electronics functional manager at BAE Systems. "The chips we designed were complex, our design process is demanding, and the specs for military-aerospace are unforgiving."

"Galaxy provided a very reliable flow, enabling the high success rate on this very challenging project," said Antun Domic, senior vice president and general manager of Synopsys' Implementation Group. "We look forward to continued participation in these demanding, cutting-edge projects."

About Synopsys

Synopsys, Inc. is the world leader in electronic design automation (EDA) software for semiconductor design. The company delivers technology-leading semiconductor design and verification platforms and IC manufacturing software products to the global electronics market, enabling the development and production of complex systems-on-chips (SoCs). Synopsys also provides intellectual property and design services to simplify the design process and accelerate time-to-market for its customers. Synopsys is headquartered in Mountain View, California and has offices in more than 60 locations throughout North America, Europe, Japan and Asia. Visit Synopsys online at https://www.synopsys.com/ .

NOTE: Synopsys is a registered trademark of Synopsys, Inc. Galaxy and Milkyway are trademarks of Synopsys. All other trademarks or registered trademarks mentioned in this release are the intellectual property of their respective owners.

SOURCE: Synopsys, Inc.

CONTACT: Robert Smith of Synopsys, Inc., +1-650-584-1261, or rsmith@synopsys.com; or Sarah Seifert of Edelman Public Relations, +1-650-429-2776, or sarah.seifert@edelman.com, for Synopsys, Inc.

Web site: https://www.synopsys.com/