

Synopsys Contributes Virtual Platform Technology to OSCI SystemC TLM 2.0 Standardization Effort

Key Technology Donated to Further OSCI Standard's Evolution

Synopsys, Inc. (NASDAQ: SNPS), a world leader in semiconductor and system level design software, today announced that it has contributed key technology to the Open SystemC Initiative (OSCI), an independent not-for-profit organization dedicated to supporting and advancing SystemC as an open source standard for system-level design. The contribution addresses the need for a high-performance interface standard to enable the development of Virtual Platforms based on SystemC.

Synopsys' contribution contains a proof-of-concept implementation, examples and collateral material. It underlines Synopsys' commitment to system-level design with SystemC. The contribution of this key technology is a proposal for the evolution of the current OSCI TLM 2.0 draft, focused at fulfilling key user requirements: increased performance, flexibility and scope.

"OSCI welcomes Synopsys' participation in the ongoing review for maturing the upcoming SystemC TLM 2.0 standard," said Alain Clouard, chairman of OSCI. "Synopsys is a founding member of OSCI and has significantly contributed to the success of SystemC. Sharing their experience in the fields of SystemC and Virtual Platform technology to support the evolution of the current TLM 2.0 draft into an OSCI industry standard is greatly appreciated, as were participations from companies partnering in the TLM WG."

"As the world leader in commercially deployed Virtual Platforms, our team has delivered high performance transaction-level solutions to our customers to enable early embedded software development," said Joachim Kunkel, vice president and general manager of the Solutions Group at Synopsys. "We strongly support the OSCI effort to create a TLM interface standard, which will accelerate the availability of interoperable, high performance system-level models suited for the development of Virtual Platforms in SystemC."

Availability

Synopsys' contribution is available to all OSCI TLM working group members immediately.

About Synopsys

Synopsys, Inc. is a world leader in EDA software for semiconductor design. The company delivers technology-leading semiconductor design and verification platforms and IC manufacturing software products to the global electronics market, enabling the development and production of complex systems-on-chips (SoCs). Synopsys also provides intellectual property and design services to simplify the design process and accelerate time-to-market for its customers. Synopsys is headquartered in Mountain View, California and has offices in more than 60 locations throughout North America, Europe, Japan and Asia. Visit Synopsys online at <https://www.synopsys.com/>.

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