Electronic Design Standards for Low Power and Analog/Custom Design to be Highlighted at 18th EDA Interoperability Forum

San Jose Mercury News High-Tech Journalist to Present Keynote

Synopsys, Inc. (NASDAQ: SNPS), a world leader in semiconductor design software, today announced that the 18th electronic design automation (EDA) Interoperability Developers' Forum will be held on November 9, 2006 in Santa Clara, CA. The Forum will feature contemporary topics of interest and controversy that are relevant to design engineers, EDA vendors, and IP providers. The Forum continues to be the premier industry event to cover a wide variety of standards and organizations, addressing challenges and solutions in the electronic design standards arena.

Mr. Dean Takahashi, business journalist with the San Jose Mercury News, will give the keynote, "The Xbox 360™ Uncloaked: The Real Story Behind Microsoft's Next-Generation Video Game Console." Mr. Takahashi has been a business journalist for 17 years, having written for the Wall Street Journal, Red Herring Magazine, Los Angeles Times, The Orange County Register, and the Dallas Times Herald. He has also appeared on CNN and CNBC.

"It is an honor to be invited to deliver the keynote at this unique industry event," said Mr. Takahashi. "The audience will get a special behind-the-scenes look at Microsoft's popular Xbox 360 that provides insight into the challenges of designing game consoles and the chips that go into them."

The Forum will feature sessions throughout the day. The morning session titled "Enabling Analog/Custom Design Interoperability" will feature a panel of industry experts in design, tool development and semiconductor manufacturing debating the state of process design kits today and the need for further standardization. Following the panel session, a group of EDA vendors will share their experience and progress in their mission to enable interoperability with OpenAccess.

"The time is now for open standards for analog/custom design. Customers have the right to select best-in-class tools. Truly open standards provide a path to interoperability and market growth," said Hau-Yung Chen, president, Silicon Canvas. "The Interoperability Developers' Forum is an ideal place to elevate industry awareness of the need for open analog/custom standards and to begin to offer real solutions."

The Forum will feature two technology tracks in the afternoon. The first track, "Power to the People" will have presentations from Accellera, Si2, and Synopsys, among others, that update the audience on progress-to-date on low power standards. It will conclude with a panel discussion on what is working and what still needs to be done to achieve standards for low power design.

"LSI's customers in the storage and consumer industries are very concerned about lowering the power requirements of their solutions. To support them, we are strongly supporting the development of a standard solution for low power design, implementation, verification, and analysis," stated Dr. Gary Delp, distinguished engineer, LSI Logic. "Events like the Interoperability Developers' Forum provide the opportunity to update the industry on progress so far and encourage everyone to participate in producing a robust Low Power standard quickly."

The second afternoon track, "Milkyway™ Digital Design Session," will focus on the MAP-inSM program which, for more than three years, has provided EDA vendors with a graphic environment, scripting, translators, and programming interfaces to the proven Synopsys Milkyway digital design database. The session will feature specific technical benefits and challenges encountered when using MAP-in to build real flows.

The day's agenda will also feature presentations on the open source Synopsys Liberty™ library standard including recent additions to the standard such as Variation-Aware Composite Current Source (CCS), Compact CCS and new low power modeling features. Highlights of the Liberty Technical Advisory Board (TAB) that was formed under Si2 earlier this year will be provided.

SystemVerilog, the unified hardware design and verification language, is being increasingly adopted by the industry. This presentation will describe the momentum of the VMM Methodology which employs the SystemVerilog standard. The audience will also receive an update on Synopsys' donation of SystemVerilog Assertion Checkers to Accellera's Open Verification Library (OVL) committee.

"Continuing in our tradition of providing the only event in the EDA industry that broadly addresses interoperability, we are bringing the audience another stellar lineup of speakers and topics," said Rich Goldman, vice president of Strategic Market Development at Synopsys. "Everyone is invited to attend and participate in this informative and interactive Forum."

About the EDA Interoperability Developers' Forum

This forum provides EDA vendors and their customers an opportunity to exchange information and ideas on EDA tool interoperability including new interface technologies, future enhancements, upcoming news, and successes. For more information and to register, visit https://www.synopsys.com/devforum/nov2006 .

About Synopsys

Synopsys, Inc. is the world leader in electronic design automation (EDA) software for semiconductor design. The company delivers technology-leading semiconductor design and verification platforms and IC manufacturing software products to the global electronics market, enabling the development and production of complex systems-on-chips (SoCs). Synopsys also provides intellectual property and design services to simplify the design process and accelerate time-to-market for its customers. Synopsys is headquartered in Mountain View, California, and has more than 60 offices located throughout North America, Europe, Japan and Asia. Visit Synopsys online at www.synopsys.com .

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