

Purdue University Receives Charles Babbage Grant From Synopsys And Sun Microsystems

Synopsys Tools Aid Microelectronic Design in New Birck Nanotechnology Center

Synopsys, Inc. (NASDAQ: SNPS), a world leader in semiconductor design software, today announced that Purdue University is the recipient of a Charles Babbage Grant. Purdue will receive 50 licenses of Synopsys electronic design automation (EDA) University Tools Package software, as well as Sun Ray™ client hardware and server software from Sun Microsystems, for their VLSI Design Laboratory. Purdue will use the Synopsys licenses for their graduate and undergraduate engineering programs in ASIC Design, as well as for leading-edge microelectronics and nanoscale research, in their new Birck Nanotechnology Center. The Birck Nanotechnology Center is considered among the best university facilities of its kind in the nation for nanotechnology research.

A wide range of Synopsys tools in use at Purdue today provides students and researchers the opportunity to utilize industrial tools for ASIC synthesis, circuit simulation, nanometer device modeling, and fabrication process modeling in both academics and research. Synopsys tools serve as the foundation for an industry standard design flow in the ASIC Design Laboratory where students get real world experience building and testing their designs.

In the Birck Nanotechnology Center, Synopsys provides device and process modeling tools for conducting nanoscale research and creating advanced devices. For VLSI and RF-MEMS research, a comprehensive set of EDA tools backed by Synopsys' support network provides a complete hardware design environment. This eliminates the difficult task of assembling a patchwork of open source and niche tools that often lack the necessary technology files and models for commercial processes.

Finding access to robust design tools with the required peripheral support for advanced research and microelectronic design is a common issue facing universities today. Synopsys solves this challenge through programs like the Charles Babbage Grant which provides select universities with design software for modern electronic design flows and leading commercial IC fabrication processes.

"Receiving the Charles Babbage Grant furthers the development and modernization goals for our VLSI Design and Engineering laboratories," said Mark C. Johnson, School of Electrical and Computer Engineering, Purdue University. "Access to leading-edge design software and compute hardware enables our engineering students to learn in the same environment as their industry counterparts, increasing the value of the student experience and research at Purdue University."

"Synopsys selected Purdue University to receive this grant because of their leadership and commitment to leading-edge microelectronic design and nanotechnology research," said Rich Goldman, vice president, Strategic Market Development at Synopsys. "Through this grant, Synopsys helps prepare students and researchers to meet the challenges of nanoscale design and build careers in the microelectronic industry."

About the Charles Babbage Grant

The Charles Babbage Grant provides select universities worldwide with state-of-the-art EDA tools, training, support and technology. It enables institutions of higher education to enhance their expertise in microelectronics circuits and system design. Use of the Synopsys tools from the grant helps universities to better prepare their graduates for the future by providing hands-on experience with current industry practices, modern design techniques, and actual design tools, enhancing their understanding of how learning applies to the real world of SoC design. The grant is named after British mathematician and inventor Charles Babbage, who designed and built mechanical computing machines on principles that anticipated the modern electronic computers of today over 150 years ago.

About Synopsys

Synopsys, Inc. is a world leader in EDA software for semiconductor design. The company delivers technology-leading semiconductor design and verification platforms and IC manufacturing software products to the global electronics market, enabling the development and production of complex systems-on-chips (SoCs). Synopsys also provides intellectual property and design services to simplify the design process and accelerate time-to-market for its customers. Synopsys is headquartered in Mountain View, California and has offices in more than 60 locations throughout North America, Europe, Japan and Asia. Visit Synopsys online at <http://www.synopsys.com>.

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