Cypress Streamlines on Synopsys Galaxy Design Platform

Galaxy Design Platform Delivering Predictable Time-to-Results Through Sign-off Correlation to Silicon

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Synopsys, Inc. (NASDAQ: SNPS), a world leader in semiconductor design software, today announced that Cypress Semiconductor Corp. (NYSE: CY), known for its performance-based process and manufacturing expertise, has signed a multiyear agreement to consolidate its digital IC design flows and methodologies using Synopsys' Galaxy™ Design Platform. Cypress will deploy the Galaxy Design Platform for RTL synthesis, design-for-test (DFT), design planning, place-and-route, timing and signal integrity sign-off, extraction, and automatic test pattern generation (ATPG).

"Our goal is to achieve first silicon success on time and within budget by deploying a design system able to consistently deliver the required performance with predictable time to results," said Paul Keswick, executive vice president of new product development at Cypress. "We all know the adage 'time is money,' and schedule slips on a key product can cost several million dollars in lost revenue. By standardizing on the proven Synopsys Galaxy Design Platform, we now have a design flow delivering predictable time-to-results across different design styles."

"We were able to reduce our total design time by as much as five weeks using Synopsys' Galaxy design platform," added Nagendra Cherukupalli, vice president of Asia Pacific design centers and chip integration at Cypress. "In order to eliminate uncertainties and achieve first silicon success, the entire design platform needs to be correlated to sign-off. The Galaxy Design Platform offers superior correlation to sign-off, allowing us to focus our efforts on design innovation and delivering first silicon on-time."

"At Synopsys, we recognize predictability, productivity, and performance are three key attributes of any successful design platform that enable the design community to bring innovative products to market quickly and cost effectively," stated Antun Domic, senior vice president and general manager of the Implementation Group at Synopsys. "We, therefore, focus our efforts on ensuring that our Galaxy Design Platform continues to deliver optimal performance and faster time-to-results while minimizing all uncertainties through unmatched correlation to sign-off. We look forward to working with Cypress to deploy the benefits of the Galaxy platform in all their digital IC design projects."

About Synopsys

Synopsys, Inc. is a world leader in EDA software for semiconductor design. The company delivers technology-leading semiconductor design and verification platforms and IC manufacturing software products to the global electronics market, enabling the development and production of complex systems-on-chips (SoCs). Synopsys also provides intellectual property and design services to simplify the design process and accelerate time-to-market for its customers. Synopsys is headquartered in Mountain View, California and has offices in more than 60 locations throughout North America, Europe, Japan and Asia. Visit Synopsys online at http:/www.synopsys.com .

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