Synopsys Expands Semiconductor IP Portfolio With Acquisition of Intrinsic ID

SUNNYVALE, Calif., March 20, 2024 /PRNewswire/ -- Synopsys, Inc. (Nasdaq: SNPS) today announced that it has completed the acquisition of Intrinsic ID, a leading provider of Physical Unclonable Function (PUF) IP used in the design of system-on-chips (SoCs). The acquisition adds production proven PUF IP to Synopsys' broadly used semiconductor IP portfolio, enabling SoC designers worldwide to protect their SoCs by generating a unique identifier on chip utilizing the inherent and distinctive characteristics of every silicon chip. The acquisition also adds Intrinsic ID's team of experienced research and development engineers who have deep expertise in PUF technology. The terms of the deal, which are not material to Synopsys financials, are not being disclosed.

"In our increasingly connected world, chip designers are integrating PUF technology in their SoCs for many applications, including identification and the creation of a product ID for track and trace," said Joachim Kunkel, general manager of the Solutions Group at Synopsys. "The acquisition of Intrinsic ID complements our extensive semiconductor IP portfolio, further helping designers create SoCs that are at the heart of today's smart and connected devices. We look forward to expanding our R&D presence in the Netherlands with Intrinsic ID's team and establishing a center of excellence for PUF technology in Eindhoven."

Synopsys is a leading provider of high-quality, silicon-proven semiconductor IP solutions for SoC designs. The broad Synopsys IP portfolio includes logic libraries, embedded memories, analog IP, wired and wireless interface IP, security IP, embedded processors and subsystems. To accelerate IP integration and silicon bring-up, Synopsys' IP Accelerated initiative provides architecture design expertise, hardening, and signal/power integrity analysis. Synopsys' extensive investment in IP quality, comprehensive technical support and robust IP development methodology enables designers to reduce integration risk and accelerate time-to-market.

About Synopsys

Catalyzing the era of pervasive intelligence, Synopsys, Inc. (Nasdaq: SNPS) delivers trusted and comprehensive silicon to systems design solutions, from electronic design automation to silicon IP and system verification and validation. We partner closely with semiconductor and systems customers across a wide range of industries to maximize their R&D capability and productivity, powering innovation today that ignites the ingenuity of tomorrow. Learn more at www.synopsys.com.

Forward-Looking Statements

This news release contains forward-looking statements, including, but not limited to, statements regarding the expected benefits of the transaction. Forward-looking statements are subject to both known and unknown risks and uncertainties that may cause actual results to differ materially from those expressed or implied in the forward-looking statements. These risks and uncertainties include, among others: Synopsys' ability to operate or integrate Intrinsic ID's assets and employees with its own successfully, which may include a potential loss of customers, key employees, partners or vendors; and uncertain customer demand and support obligations for silicon-proven hardware- and software-based PUF IP. Other risks and uncertainties that may apply are set forth in the Risk Factors section of Synopsys' most recently filed Quarterly Report on Form 10-Q. Synopsys assumes no obligation to update any forward-looking statement contained in this news release.

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