Calterah Semiconductor's Automotive Radar SoC Enters Mass Production Using Synopsys DesignWare ARC Processor IP

ISO 26262 ASIL Compliant ARC EM Processor IP Enables Calterah to Accelerate Safety Certification for Next-Generation Millimeter Wave Radar Chip

MOUNTAIN VIEW, Calif., and SHANGHAI, Dec. 12, 2019 /PRNewswire/ --

Highlights:

- Calterah's new Alps chip series includes four transmitter channels at most, four receiver channels, a highly configurable waveform generator, and an integrated analog-to-digital converter with sampling rates up to 50 MSPS
- Calterah selected Synopsys' ASIL-compliant ARC EM6 Processor and ARC MetaWare Development Toolkit for Safety to simplify the hardware and software development of its safety-critical automotive SoC

Synopsys, Inc. (Nasdaq: SNPS) and Calterah Semiconductor today announced that Calterah's new-generation advanced CMOS millimeter wave (MMW) radar system-on-chip (SoC) integrating Synopsys' DesignWare® ARC® EM Processor IP has entered mass production. Calterah implemented an ISO 26262 functional safety development process to help ensure the Alps chip series met the standard's ASIL B requirements for automotive safety. This included conducting functional safety-related monitoring and detection on more than 190 blocks in the design and analyzing over 1,000 failure modes, leading to the development of dozens of chip-level safety mechanisms. By using Synopsys' ASIL-compliant ARC EM6 Processor, Calterah successfully reached its target functional safety ASIL level while enhancing overall processing performance and power efficiency for its Alps automotive radar SoC.

"Calterah's new-generation Alps 77/79 GHz chip series boasts higher speed, flexibility, user-friendliness and reliability to deliver more competitive chip solutions for automotive millimeter wave radar," said Hongquan Liu, chief marketing officer at Calterah Semiconductor. "With Synopsys' functional safety-compliant ARC EM Processor IP, Calterah will continue providing its global users with higher-performance, easier-to-use and lower-power MMW radar technology to create a safer, smarter environment for drivers and their passengers."

Calterah's Alps chip series includes four transmitter channels at most, four receiver channels, a highly configurable waveform generator, and an analog-to-digital converter with sampling rates of up to 50 million of samples per second (MSPS). The complete, efficient signal processing baseband implements classical radar algorithms in hardware, saving development resources and delivering a solution with lower power consumption and higher performance. In addition to the conventional embedded wafer-level ball grid array (eWLB) package, the Alps chip series also includes an Antenna in Package (AiP) solution, which greatly reduces the difficulty and cost of radar development by integrating the antenna onto the chip packaging layer. This diverse lineup of next-generation products provides users with a complete set of solutions covering long-range, medium-range, short-range, and ultra-short-range radar solutions.

Synopsys offers a broad portfolio of automotive-grade DesignWare IP that is ASIL ready, is ISO 26262 certified, meets stringent AEC-Q100 reliability standards, and supports automotive quality management to help accelerate the development of ADAS, connected vehicle and infotainment, and MCU designs. The ASIL-compliant DesignWare ARC EM Processor integrates hardware safety features and safety monitors to detect system errors.

"Advanced automotive ADAS SoCs require massive amounts of data processing to accommodate a range of functions such as radar, LiDAR and sensor," said John Koeter, senior vice president of marketing for IP at Synopsys. "Synopsys provides companies such as Calterah with the industry's broadest portfolio of silicon-proven, ASIL Ready IP including differentiated ARC Processors that address the functional safety, reliability and quality requirements of automotive systems."

Additional Resources

- Learn more about Calterah's Alps chip series
- Learn more about Synopsys DesignWare ARC Functional Safety Processors

About Calterah Semiconductor

Calterah Semiconductor is dedicated to the development and design of CMOS mmWave radar sensor ICs, targeting applications in ADAS, automated driving, security screening and imaging, smart home, and other fields. It is the only enterprise of China that has achieved mass production of 77/79-GHz mmWave radar sensor chips so far. Calterah's first generation of chips were mass-produced in 2017, including a 77/79-GHz CMOS mmWave radar transceiver—Yosemite, and a 60-GHz

CMOS mmWave radar transceiver—Yellowstone. Yosemite, also known as the world's first mass-produced 77/79-GHz CMOS mmWave radar sensor chip, has now been successfully introduced into quite a few automotive Tier 1 and sensor manufacturers. The 60-GHz Yellowstone has also gained broad attention in the applications in fields like transportation, security, imaging, etc. In 2019, Calterah rolled out 77/79-GHz CMOS mmWave radar SoC family—Alps. This 2nd generation of products provides customers with easier-to-use, higher-performance, and lower-power radar solutions. Another brand-new 60-GHz SoC product—Rhine, is also coming along. For more information, please visit http://www.calterah.com/.

About Synopsys

Synopsys, Inc. (Nasdaq: SNPS) is the Silicon to Software™ partner for innovative companies developing the electronic products and software applications we rely on every day. As the world's 15th largest software company, Synopsys has a long history of being a global leader in electronic design automation (EDA) and semiconductor IP and is also growing its leadership in software security and quality solutions. Whether you're a system-on-chip (SoC) designer creating advanced semiconductors, or a software developer writing applications that require the highest security and quality, Synopsys has the solutions needed to deliver innovative, high-quality, secure products. Learn more at www.synopsys.com.

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