The MOSIS Service Selects Synopsys' IC Validator for Large-scale FinFET SoCs

IC Validator Delivers Superior Performance for DRC and LVS Signoff

MOUNTAIN VIEW, Calif., July 11, 2019 /PRNewswire/ --

Highlights:

- IC Validator ultra-scalable physical verification solution provides significant runtime advantage
- The MOSIS Service deploys IC Validator for DRC and LVS signoff on leading FinFET process designs

Synopsys, Inc. (Nasdaq: SNPS) today announced that The MOSIS Service, a leading provider of Multi-Project Wafers, has selected Synopsys' IC Validator tool for physical verification signoff. IC Validator's feature-rich physical verification solution, coupled with a highly scalable engine, has allowed The MOSIS Service to achieve significantly faster physical signoff. The MOSIS Service deployed IC Validator for full-chip design rule checking (DRC) and layout-versus-schematic (LVS) signoff on designs in FinFET process technologies.

"The MOSIS Service processes designs for a high volume of multi-project wafers that speed production and reduce costs for our customers. We require a highly productive physical verification solution to ensure on-time delivery of design tapeouts," said James Whalen, co-director of The MOSIS Service. "IC Validator has enabled our engineers with productivity and performance functionality to sign off designs on-time."

IC Validator, a key component of Synopsys' Fusion Design Platform™, is a comprehensive and highly scalable physical verification tool suite including DRC, LVS, programmable electrical rule checks (PERC), dummy metal fill, and design-for-manufacturability (DFM) enhancement capabilities. IC Validator is architected for high performance and scalability that maximizes utilization of mainstream hardware, using smart memory-aware load scheduling and balancing technologies. It uses both multi-threading and distributed processing over multiple machines to provide scalability benefits that extend to more than a thousand CPUs.

"At advanced process technologies, physical verification closure within schedule has become a challenge because of the increasing manufacturing complexity," said Dan Page, vice president, Design Group at Synopsys. "Through high performance, scalability, and readily available optimized runsets from all major foundries, IC Validator is providing designers with the fastest path to production silicon."

About Synopsys

Synopsys, Inc. (Nasdaq: SNPS) is the Silicon to Software™ partner for innovative companies developing the electronic products and software applications we rely on every day. As the world's 15th largest software company, Synopsys has a long history of being a global leader in electronic design automation (EDA) and semiconductor IP and is also growing its leadership in software security and quality solutions. Whether you're a system-on-chip (SoC) designer creating advanced semiconductors, or a software developer writing applications that require the highest security and quality, Synopsys has the solutions needed to deliver innovative, high-quality, secure products. Learn more at www.synopsys.com.

Editorial Contact:

James Watts
Synopsys, Inc.
650-584-1625
jwatts@synopsys.com

SOURCE Synopsys, Inc.