

# Synopsys Users Group India Celebrates 19th Year with Record Gathering of the Semiconductor Design Community

BENGALURU, India, July 11, 2018 /PRNewswire/ -- [Synopsys, Inc.](#) (Nasdaq: SNPS) today announced it will host its 19th annual Synopsys Users Group (SNUG™) conference July 11-12, 2018 at the Leela Palace in Bengaluru, India. The two-day conference focuses on helping engineers better address productivity challenges when designing chips and electronic systems. This year's event will feature new electronic design automation (EDA) solutions and techniques to meet the challenges posed by 5-nanometer technology nodes and below.

SNUG India 2018 will feature more than 70 user papers and presentations on design synthesis, low-power implementation and verification, test, mixed-signal verification, IP integration, emulation, virtual prototyping, and FPGA-based prototyping. The event has become the industry's premier gathering of semiconductor professionals in the region for sharing best practices and methodologies in semiconductor design. SNUG India is part of a global Synopsys program that began in 1991 and currently includes 15 user conferences worldwide with more than 10,000 participants annually.

Dr. Pradip K. Dutta, group vice president and managing director of Synopsys India and Sri Lanka, said "As we move closer to completing two decades of SNUG in India, we are committed to making it the leading platform for the electronic design engineering community to connect with each other and learn to innovate from silicon to software. As the semiconductor industry and the ecosystem evolve with the accelerating pace of digital transformation, the center of gravity is moving to the intersection of hardware and software," added Dutta.

## Keynote Presentations

Dr. Aart de Geus, chairman and co-chief executive officer at Synopsys, will discuss in his keynote address, "At the Heart of Impact," how the demand for computation power is changing virtually all industries from automotive to healthcare to financial services. He will explore the trends, opportunities, and emerging solutions in the new exciting landscape.

"All market segments are digitizing themselves, and the semiconductor ecosystem is at the center of making this happen," said Aart de Geus. "This transformation is changing and challenging the semiconductor industry as we evolve to a world of Smart Everything that is networked, mobile, and also under increased pressure to become more secure. Synopsys is both humbled and privileged to be in the midst of this transformation."

Pradeep Elamanchili, vice president, global ASIC engineering, Western Digital, will deliver a keynote on- "Data-Centric World, Opportunities, and Insights". He will explore the evolution of big and fast data and technologies that will enable next generation of innovation in Machine Learning and Artificial Intelligence.

"Today, enterprises are seeing exponential increase in volume, variety and longevity of Big and Fast Data. Machine Learning and AI rely on this data to capture customers' attention and thereby the market share," further added Elamanchili. "Western Digital is committed to working with our partners to create technologies and platforms that foster new discoveries through transformative value of data."

SNUG India 2018 will also feature the participation of industry ecosystem partners, including Platinum Sponsors Arm, GLOBALFOUNDRIES, Samsung, and TSMC.

## About SNUG (Synopsys Users Group)

The Synopsys Users Group (SNUG) was established in 1991 to provide users of Synopsys design tools, IP, and technology with an open forum in which they can exchange ideas, discuss problems, and explore solutions. SNUG has emerged as the largest community of design engineers in the world, with 15 SNUG conferences worldwide drawing more than 10,000 designers and managers each year. Attendees represent the world's largest semiconductor design and manufacturing companies as well as many innovative start-ups. At the heart of each SNUG event is a technical conference focusing on real-world design challenges and featuring peer-reviewed user papers, along with presentations from top Synopsys technologists and industry experts. For more information, visit <https://www.synopsys.com/community/snug.html>.

## About Synopsys

Synopsys, Inc. (Nasdaq: SNPS) is the Silicon to Software™ partner for innovative companies developing the electronic products and software applications we rely on every day. As the world's 15th largest software company, Synopsys has a long history of being a global leader in electronic design automation (EDA) and semiconductor IP and is also growing its leadership in software security and quality solutions. Whether you're a system-on-chip (SoC) designer creating advanced semiconductors, or a software developer writing applications that require the highest security and quality, Synopsys has the solutions needed to deliver innovative, high-quality, secure products. Learn more at [www.synopsys.com](http://www.synopsys.com).

### Editorial Contacts:

Dipa Joisher  
Madison PR  
91 98926-61041  
[dipa.joisher@madisonpr.in](mailto:dipa.joisher@madisonpr.in)

Simone Souza  
Synopsys, Inc.  
650-584-6454  
[simone@synopsys.com](mailto:simone@synopsys.com)

---