Synopsys Test Platform Tools Certified for the Most Stringent Level of Automotive Safety Measures Defined by the ISO 26262 Standard

Provides Highest Degree of Safety Related Confidence and Accelerates Functional Safety Qualification

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Highlights:

- All Synopsys test platform products can be confidently used for functionally safe automotive systems
- Test platform tools, tool qualification report and IP certification accelerate ISO 26262 functional safety qualification for automotive ICs up to the most stringent safety requirements for ASIL D
- Synopsys test platform offers all required components for advanced manufacturing test and in-system self-test for safety-critical automotive ICs

Synopsys, Inc. (NASDAQ: SNPS), today announced that its test platform tools for high-quality manufacturing test are completely certified for the ISO 26262 automotive functional safety standard. To achieve this, SGS-TÜV Saar GmbH, an independent accredited assessor, recently certified the tool qualification reports for the DFTMAX™, DFTMAX Ultra, and DFTMAX LogicBIST solutions. These certifications are in addition to previously certified Synopsys test products that include TetraMAX® II ATPG solution, DesignWare STAR Memory System® and STAR Hierarchical System. Certification provides designers the highest level of confidence in the use of Synopsys' test solution for safety-critical automotive applications and accelerates functional safety qualification for automotive ICs, up to the stringent requirements for ASIL D.

Automakers are increasingly deploying complex electronic safety systems, or advanced driver-assistance systems, to monitor and control in-vehicle safety-critical functions. Since failures in these systems may lead to unacceptable consequences, automakers work with their suppliers to increase IC quality and reliability and to ensure the methods and tools used during design and maintenance processes are compliant with safety standards. The ISO 26262 standard outlines requirements that must be taken into consideration while developing a functionally safe automotive IC, including qualification requirements for design tools. SGS-TÜV Saar has certified Synopsys' test platform tools, tool qualification reports, and IP for ASIL D, the highest level of functional safety requirements prescribed by the ISO 26262 standard.

"Reducing risk is critical to ensure functional safety for automotive electronic systems, such as advanced driver-assistance systems," said Gudrun Neumann, head of functional safety software at SGS Group, SGS-TÜV Saar GmbH. "For safety-critical automotive applications, designer confidence in tools and IP rises when they satisfy the qualification requirements of the ISO 26262 automotive functional safety standard. We issued certificates to Synopsys test platform tools and tool qualification reports based on a successful evaluation against the requirements of ISO 26262."

"Achieving functional safety is a growing challenge for automotive IC suppliers," saidBijan Kiani, vice president of marketing in Synopsys' Design Group. "We are committed to reducing the time and effort for our customers to meet this challenge. ISO 26262 certification of our test platform further reinforces our commitment to enable designers to efficiently meet their automotive IC's functional safety requirements related to manufacturing test quality and in-system test."

About the Synopsys Test Platform

The Synopsys test platform is comprised of DFTMAX Ultra, DFTMAX, TetraMAX and TetraMAX II technologies for power-aware logic test and physical diagnostics; DFTMAX LogicBIST for in-system self-test; SpyGlass® DFT ADV for testability analysis; the DesignWare® STAR Hierarchical System for automated hierarchical testing of analog/mixed-signal IP, digital logic blocks, memory and interface IP on an SoC; the DesignWare STAR Memory System® for embedded test, repair and diagnostics; and Yield Explorer design-centric yield analysis. Synopsys' test solution combines Design Compiler® RTL synthesis with embedded test technology to optimize timing, power, area and congestion for test as well as functional logic, leading to faster time-to-results. The Synopsys test solution delivers tight integration across the Synopsys Design Platform, including Design Compiler synthesis, IC Compiler™ II place and route, and PrimeTime timing analysis, to enable faster turnaround time while meeting both design and test goals, higher defect coverage and faster yield ramp.

About Synopsys

Synopsys, Inc. (Nasdaq: SNPS) is the Silicon to Software™ partner for innovative companies developing the electronic products and software applications we rely on every day. As the world's 15th largest software company, Synopsys has a long history of being a global leader in electronic design automation (EDA) and semiconductor IP and is also growing its leadership

in software security and quality solutions. Whether you're a system-on-chip (SoC) designer creating advanced semiconductors, or a software developer writing applications that require the highest security and quality, Synopsys has the solutions needed to deliver innovative, high-quality, secure products. Learn more at www.synopsys.com.

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