Synopsys Expands DesignWare IP Portfolio with Acquisition of Sidense Corporation

Acquisition Adds One-Time Programmable Non-Volatile Memory IP to DesignWare NVM IP Offering

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Highlights

- Sidense's one-time programmable (OTP) non-volatile memory (NVM) IP offers a robust standard CMOS implementation, small footprint, low power, and broad foundry support
- Sidense's OTP NVM IP complements Synopsys' existing multi-time programmable (MTP)-based NVM solutions
- Acquisition expands DesignWare NVM IP solution to support 16-bit to 1.28-Mbit instances
- Highly secure, field programmable OTP NVM IP is used in many applications including fuse replacement, secure key storage, device ID, analog trim, and code storage

Synopsys, Inc. (Nasdaq: SNPS) today announced that it has acquired Sidense Corporation, a leading provider of one-time programmable (OTP) non-volatile memory (NVM) for automotive, mobile, industrial and Internet of Things (IoT) applications. The acquisition complements Synopsys' existing DesignWare® Multi-Time Programmable (MTP) NVM IP solution with OTP NVM IP in 16-bit to 1.28-Mbit configurations. With this acquisition, Synopsys gains access to proven, market-leading OTP NVM IP in process technologies from 180- to 16-nm along with a team of highly experienced R&D engineers.

The terms of the deal, which is not material to Synopsys financials, have not been disclosed.

"Sidense's OTP NVM IP offers designers a secure, area-efficient solution for fuse replacement, secure key storage, device ID, analog trim, and code storage," said Joachim Kunkel, general manager of the Solutions Group at Synopsys. "By adding OTP NVM from Sidense to our DesignWare IP portfolio, Synopsys provides designers with a broader NVM IP solution that offers small area, fast access times and high reliability for their SoC designs."

Sidense's embedded one-transistor OTP technology is based on its patented split-channel 1T-Fuse™ bit-cell architecture that provides better yield, higher security, improved reliability and lower overall product cost. Sidense's antifuse-based OTP NVM technology offers ultra-low power, small area NVM IP with read access times as fast as 10 nanoseconds. The OTP NVM IP can be manufactured in standard-logic CMOS fabrication processes and does not require any additional mask layers or process steps. The IP is in high-volume production in automotive, mobile and industrial applications.

About DesignWare IP

Synopsys is a leading provider of high-quality, silicon-proven IP solutions for SoC designs. The broad DesignWare® IP portfolio includes logic libraries, embedded memories, embedded test, analog IP, wired and wireless interface IP, security IP, embedded processors and subsystems. To accelerate prototyping, software development and integration of IP into SoCs, Synopsys' IP Accelerated initiative offers IP prototyping kits, IP software development kits and IP subsystems. Synopsys' extensive investment in IP quality, comprehensive technical support and robust IP development methodology enable designers to reduce integration risk and accelerate time-to-market. For more information on DesignWare IP, visit www.synopsys.com/designware.

About Synopsys

Synopsys, Inc. (Nasdaq: SNPS) is the Silicon to Software™ partner for innovative companies developing the electronic products and software applications we rely on every day. As the world's 15th largest software company, Synopsys has a long history of being a global leader in electronic design automation (EDA) and semiconductor IP and is also growing its leadership in software security and quality solutions. Whether you're a system-on-chip (SoC) designer creating advanced semiconductors, or a software developer writing applications that require the highest security and quality, Synopsys has the solutions needed to deliver innovative, high-quality, secure products. Learn more at www.synopsys.com.

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