TSMC Recognizes Synopsys with Four Partner Awards at the Open Innovation Platform Forum Event

TSMC Awards recognize Synopsys' Collaboration on Interface IP, Joint Development of 7nm FinFET Plus and 12FFC Design Infrastructures and Joint Delivery of an Automotive Design Enablement Platform

MOUNTAIN VIEW, Calif., Sept. 18, 2017 /PRNewswire/ --

Highlights:

- TSMC selects Synopsys as its "Partner of the Year" for interface IP and tool enablement for the 7th consecutive year
- TSMC has certified Synopsys' digital and custom implementation platforms and reference flows for the Synopsys Design Platform, including the IC Compiler™ II solution for 7nm FinFET Plus and 12FFC design infrastructures and automotive design enablement
- Portfolio of Synopsys DesignWare® Interface IP, including USB-C 3.1/3.0/2.0, DisplayPort, PCI Express® 4.0, CCIX, Multi-Protocol 25G, DDR4, LPDDR4, MIPI® and HDMI, is now available for TSMC 16-nm, 12-nm and 7nm FinFET processes

Synopsys, Inc. (Nasdaq: SNPS), today announced that TSMC has recognized Synopsys with four "2017 Partner of the Year" awards for Interface IP and joint development of 7nm FinFET Plus and 12FFC design infrastructures, plus joint delivery of an automotive design enablement platform. Synopsys and TSMC have collaborated for more than 17 years, most recently to accelerate the adoption of FinFET technology for optimum power, performance and area for the 7nm FinFET Plus and 12-nm processes. This is the 7th consecutive year Synopsys has received both IP and electronic design automation (EDA) accolades from TSMC.

"We are honored to receive these prestigious awards from TSMC," said Dr. Michael Jackson, corporate vice president of Synopsys' Design Group. "Through this kind of deep engineering collaboration with TSMC on its 7-nanometer FinFET Plus and 12FFC FinFET processes, automotive design enablement platform and interface IP, we have enabled a state-of-the-art design platform and IP portfolio that enables designers to achieve their time-to-market goals."

"We have presented the 2017 Partner of the Year award to Synopsys in recognition of our collaboration to deliver important innovations in the field of semiconductor design," said Suk Lee, TSMC's senior director, Design Infrastructure Marketing Division. "With its extensive high-quality DesignWare IP portfolio and certified Design Platform, Synopsys enables companies to achieve their design goals and accelerate production."

About Synopsys

Synopsys, Inc. (Nasdaq: SNPS) is the Silicon to Software™ partner for innovative companies developing the electronic products and software applications we rely on every day. As the world's 15th largest software company, Synopsys has a long history of being a global leader in electronic design automation (EDA) and semiconductor IP, and is also growing its leadership in software security and quality solutions. Whether you're a system-on-chip (SoC) designer creating advanced semiconductors, or a software developer writing applications that require the highest security and quality, Synopsys has the solutions needed to deliver innovative, high-quality, secure products. Learn more at www.synopsys.com.

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