# Technology Momentum Extends Synopsys IC Compiler II Deployments Past 100 Customers

Superior Quality of Results and Throughput Drive the Fastest Product Ramp-up in Synopsys History

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**Highlights:**

- IC Compiler II is deployed at more than 100 customers, including 14 of the top 15 IC design companies worldwide
- Since the first tapeout two years ago, IC Compiler II has successfully taped out more than 1,000 physical partitions, cementing its leadership position in the place-and-route user base
- Continuing technology enhancements stand to strengthen the quality-of-results (QoR) advantage of IC Compiler II and further boost adoption
- Exemplifying this adoption trend, the upcoming Technology Symposium will feature notable IC design leaders including Mellanox, Movidius, Samsung and others, as well as design ecosystem partners ARM and TSMC

Synopsys, Inc. (Nasdaq: SNPS) today announced that its IC Compiler™ II place-and-route system has surpassed the deployment landmark of 100 customers. Launched two years ago as the successor to IC Compiler (the market-leading solution for advanced designs), IC Compiler II has consistently demonstrated impressive turnaround time and QoR advantages, motivating industry leaders to aggressively standardize on it. IC Compiler II is currently deployed at 14 of the top 15 integrated circuit (IC) design houses; it has grown to become the primary solution for 11 of these customers. IC Compiler II has now enabled successful implementation of more than 1,000 physical partitions in customer tapeouts across 21 different emerging and established process nodes.

Concurrent with achieving this significant milestone, Synopsys is hosting a one-day Technology Symposium inSanta Clara, Calif. on September 14, 2016. The Symposium will feature presentations by leaders in the design community who will offer insights into current design challenges and how they are addressing them with IC Compiler II. More information about this event is available here.

"Our close collaboration with Synopsys on IC Compiler II has had a profound effect on our speed in turning around design changes for our vision processors in the market," said Sean Mitchell, senior vice president and chief operating officer at Movidius. "IC Compiler II is a truly exciting advancement that helps us meet the ever-shrinking development schedules and power dissipation goals for our differentiated vision processor platform."

"At Mellanox, we are constantly pushing the technology envelope and looking to improve productivity to enable on-time delivery of our world-leading data center smart interconnect solutions," said Tzvika Shmueli, vice president of back-end chip design at Mellanox. "We have chosen to deploy IC Compiler II for our projects as the speed and QoR benefits enable us to address schedule needs."

Synopsys continues to deploy new IC Compiler II technology innovations, extending its QoR and turnaround time leadership. In the latest release, several new technologies have been rolled out, including Advanced Rule-Aware Optimization, Top-Level Global-Route-Based Buffering and Automated Clock-SI Reduction. Together, these technologies offer material gains in QoR by improving timing, area and power for advanced-node designs. In addition, new optimization techniques such as Global-Route-Driven Predictive Pre-Route Optimization provide significant gains in placement QoR and further improve pre- to post-route correlation. Novel buffering approaches enhance power and area QoR by reducing parallelized branches and making intelligent use of available cells. Signoff correlation and design convergence also benefit from recent enhancements to the PrimeTime® delay calculation and availability of the StarRC™ extraction tool within IC Compiler II. For advanced-node designs, IC Compiler II offers state-of-the-art features to address 7-nanometer technology requirements throughout the infrastructure.

"Since the launch of IC Compiler II in 2014, customer adoption has been faster than any product in Synopsys history, and the impact of this new technology profound," said Antun Domic, executive vice president and general manager of the Design Group at Synopsys. "More than 100 logos, more than 1,000 blocks taped out, nearly two dozen process nodes, all in two years: these are landmark achievements that underscore the strength of our technology and further reinforce our commitment to delivering innovative solutions that enable our customers' success."

**About Synopsys**

Synopsys, Inc. (Nasdaq: SNPS) is the Silicon to Software™ partner for innovative companies developing the electronic products and software applications we rely on every day. As the world's 15th largest software company, Synopsys has a long

history of being a global leader in electronic design automation (EDA) and semiconductor IP, and is also growing its leadership in software quality and security solutions. Whether you're a system-on-chip (SoC) designer creating advanced semiconductors, or a software developer writing applications that require the highest quality and security, Synopsys has the solutions needed to deliver innovative, high-quality, secure products. Learn more at www.synopsys.com.

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