

MEDIA ADVISORY/ALERT: Synopsys to Showcase Complete Silicon to Software Solutions at DAC 2016

MOUNTAIN VIEW, Calif., June 2, 2016 /PRNewswire/ -- Synopsys, Inc. (Nasdaq: SNPS), will showcase its complete silicon to software solutions at the Design Automation Conference (DAC) 2016 in Austin, Texas, June 5 – 9, 2016. The program will include special events and presentations featuring technology experts from leading semiconductor companies, IP providers and foundries, as well as Synopsys. Topics will spotlight the best practices, tools and methodologies that are being used to design the next generation of electronics and software.

Synopsys DAC Events, Monday, June 6, 2016

WHAT: TSMC-ARM-Synopsys Breakfast

TOPIC: Collaborating to Enable Design with the Latest Processors and FinFET Processes

TIME: 7:15 a.m. to 8:45 a.m.

LOCATION: Hilton Hotel, 6th Floor, Austin Grand Ballroom H

For more information please visit:

<https://www.synopsys.com/Community/Partners/TSMC/Pages/dac-2016-tsmc-breakfast.aspx>

WHAT: IC Compiler™ II Luncheon

TOPIC: Achieving Industry-best QoR on Advanced Designs

TIME: 11:30 a.m. to 1:30 p.m.

LOCATION: Hilton Hotel, 6th Floor, Austin Grand Ballroom H

For more information please visit:

<https://www.synopsys.com/Tools/Implementation/PhysicalImplementation/Pages/dac-2016-ic-compiler-ii-lunch.aspx>

WHAT: Circuit Simulation Luncheon

TOPIC: Robust AMS Design Verification at Advanced Nodes

TIME: 11:30 a.m. to 1:30 p.m.

LOCATION: Hilton Hotel, 6th Floor, Austin Grand Ballroom G

For more information, please visit:

<https://www.synopsys.com/Tools/Verification/AMSVerification/Pages/dac-2016-ams-verification-luncheon.aspx>

WHAT: PrimeTime® Special Interest Group (SIG) Dinner

TOPIC: Work Smarter with Advanced Signoff Technology

TIME: 6:00 p.m. to 7:30 p.m.

LOCATION: Brazos Hall

For more information please visit:

<https://www.synopsys.com/Tools/Implementation/SignOff/PrimeTime/Pages/primetime-sig-dac-2016.aspx>

Synopsys DAC Events, Tuesday, June 7, 2016

WHAT: Samsung-Synopsys Breakfast

TOPIC: Ready to Design at 10nm! Synopsys and Samsung Foundry 10nm Enablement for Tapeout

Success

TIME: 7:15 a.m. to 8:45 a.m.

LOCATION: Hilton Hotel, 6th Floor, Austin Grand Ballroom G

For more information please visit:

<https://www.synopsys.com/Community/Partners/samsung/Pages/dac-2016-samsung-breakfast.aspx>

WHAT: Verification Luncheon

TOPIC: SoC Leaders Verify with Synopsys

TIME: 11:30 a.m. to 1:30 p.m.

LOCATION: Hilton Hotel, 6th Floor, Austin Grand Ballroom H

For more information please visit:

<https://www.synopsys.com/Tools/Verification/Pages/dac2016-verification-luncheon-panel.aspx>

WHAT: Custom Compiler™ Luncheon

TOPIC: Cutting Layout Tasks from Days to Hours

TIME: 11:30 a.m. to 1:30 p.m.

LOCATION: Hilton Hotel, 6th Floor, Austin Grand Ballroom G

For more information please visit:

<https://www.synopsys.com/Tools/Implementation/CustomImplementation/Pages/dac-2016-custom-design-luncheon.aspx>

WHAT: GLOBALFOUNDRIES-Synopsys Dinner

TOPIC: What's Important for IoT – Power, Performance or Integration...or All of the Above?

TIME: 6:15 p.m. to 8:15 p.m.

LOCATION: Hilton Hotel, 6th Floor, Austin Grand Ballroom G

For more information, please visit:

<https://www.synopsys.com/Community/Partners/GlobalFoundries/Pages/dac-2016-globalfoundries-dinner.aspx>

Synopsys at DAC 2016

Visit booth #149 to learn how Synopsys can help you meet your electronic design and software development goals. Our experts will be available to answer your questions and the Synopsys Silicon to Software Theater will feature thought leaders speaking about key innovations and trends impacting the semiconductor industry. The Synopsys team will also be available to meet with you in many of our partners' booths. Whether you're an SoC designer creating advanced semiconductors, or a software developer writing applications that require the highest quality and security, Synopsys has the solutions needed to deliver innovative, high-quality, secure products.

For more information on all of Synopsys' activities at DAC, visit: www.synopsys.com/DAC.

Media Registration Information

Seating is limited and registration is required. Members of the media interested in attending Synopsys' DAC special events or requiring more information should contact Tess Berdiago-Cahayag at maritess@synopsys.com.

About Synopsys

Synopsys, Inc. (Nasdaq:SNPS) is the Silicon to Software™ partner for innovative companies developing the electronic products and software applications we rely on every day. As the world's 15th largest software company, Synopsys has a long history of being a global leader in electronic design automation (EDA) and semiconductor IP and is also growing its leadership in software quality and security solutions. Whether you're a system-on-chip (SoC) designer creating advanced semiconductors, or a software developer writing applications that require the highest quality and security, Synopsys has the solutions needed to deliver innovative, high-quality, secure products. Learn more at www.synopsys.com.

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