

Synopsys Extends Verification IP Portfolio for Automotive Applications

Native SystemVerilog VIP and Source Code Test Suites for CAN 2.0/FD/TT, LIN, FlexRay and Ethernet AVB

MOUNTAIN VIEW, Calif., April 5, 2016 /PRNewswire/ -- Synopsys, Inc. (NASDAQ: SNPS), today announced the expansion of its VC Verification IP (VIP) portfolio with CAN 2.0/FD/TT (ISO 16845 compliant), LIN, FlexRay™, and Ethernet Audio Video Bridging (AVB) protocols for the verification of automotive applications. These verification IP titles include test suites and spec-linking technology required to verify compliance to automotive standards. With the increase in demand for state-of-the-art integrated electronics in automobiles, Synopsys' comprehensive VIP portfolio enables system-on-chip (SoC) teams to design next-generation electronic systems for safe, secure, smart, and connected automobiles.

Synopsys VC VIP enables SoC teams to design next-generation automotive chips with ease of use, fast integration and optimum performance. Synopsys source code test suites for protocol features, spec-linking and coverage enable SoC teams to accelerate verification closure of automotive designs with critical requirements of compliance, quality and time to market. Synopsys VIP is natively integrated with the Verdi® Protocol Analyzer debug solution and features advanced debug ports. The VIP also features error injection capabilities, built-in-protocol checks, built-in coverage and verification plans.

"We continue to expand our VIP portfolio with new products for the automotive segment, as protocols are developed and standardized," said Vikas Gautam, group director of VIP R&D and corporate applications for Synopsys' Verification Group. "With the introduction of Synopsys VIP for CAN 2.0/FD/TT, LIN, FlexRay, and Ethernet AVB, we provide our customers with advanced capabilities for verification of automotive SoC designs."

Availability & Resources

Synopsys VC VIP for CAN 2.0/FD/TT, LIN, FlexRay and Ethernet AVB are available today as standalone products, and as part of the Synopsys VIP Library.

Synopsys' portfolio of [DesignWare® automotive-grade IP](#) and [virtual prototyping](#) solutions is also available today.

About Synopsys

Synopsys, Inc. (NASDAQ:SNPS) is the Silicon to Software™ partner for innovative companies developing the electronic products and software applications we rely on every day. As the world's 16th largest software company, Synopsys has a long history of being a global leader in electronic design automation (EDA) and semiconductor IP and is also growing its leadership in software quality and security solutions. Whether you're a system-on-chip (SoC) designer creating advanced semiconductors, or a software developer writing applications that require the highest quality and security, Synopsys has the solutions needed to deliver innovative, high-quality, secure products. Learn more at www.synopsys.com.

Editorial Contact:

Sheryl Gulizia

Synopsys, Inc.

650-584-8635

sgulizia@synopsys.com

