Synopsys' IC Compiler II Accelerates Silicon Validation of Imagination's Ground-breaking PowerVR® Ray Tracing IP

Imagination's IMGworks Platform IP Group Leverages Long Standing Collaboration with Synopsys to Make Full Use of IC Compiler II's Advanced Capabilities

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Highlights:

- IC Compiler II's game-changing capacity, turnaround time and productivity help shrink Imagination's IMGworks Platform IP Group schedules from concept to silicon
- Mutual customers to benefit from Imagination's fully validated IC Compiler II reference flows in future DOK's (Design Optimization Kits)
- Up to 10X improvement in design planning throughput (compared to previous production environment) and accelerated block implementation help deliver best-in-class SoC implementation flow

Synopsys, Inc. (Nasdaq: SNPS) today announced that Imagination Technologies has successfully used IC Compiler™ II place and route solution as part of the design flow used to tape out its latest PowerVR Wizard ray tracing graphics processor test silicon, based on Imagination's reference design.

The long-standing strategic collaboration between Synopsys and Imagination has played a key role in the development of IC Compiler II, and its deployment within Imagination. Imagination's IMGworks Platform IP Group has already successfully used IC Compiler II as part of the design flow for their leading-edge PowerVR Rogue family of industry-leading GPU IP cores, seeing a substantial acceleration in its design-planning flow compared to the previous production environment.

"Our close collaboration with Synopsys since the early release of IC Compiler II has had a significant impact on how we are developing and delivering our Design Optimization Kits for our latest PowerVR, MIPS and Ensigma IP cores," said Mark Dunn, EVP of the IMGworks Platform IP Group at Imagination. "Using IC Compiler II is enabling us to offer more effective design flows and services to help our mutual customers designing complex SoCs with our broad IP portfolio."

Launched last year, IC Compiler II is the successor to IC Compiler, the industry's leading place and route solution for advanced design across both established and emerging nodes. IC Compiler II is a modern place and route system designed and built from the ground up for speed to deliver an overall 10X improvement in designer throughput and productivity. IC Compiler II was built to deliver a major leap forward in physical design throughput and productivity across all process nodes. Its natively architected multi-everything infrastructure and multicore technology enables ultra-high-capacity design planning capabilities and also serves as a scalable foundation for its unique, patented clocking technology and advanced global, analytical design closure techniques which deliver industry leading quality-of-results and fastest time to market. With high levels of automation, fast user feedback, incremental and convergent engines, IC Compiler II helps manage the many complexities of design planning and implementation. Through these industry-first innovations, IC Compiler II is offering a world of new opportunities by delivering 5X faster runtime along with half the iterations required to achieve the target performance, together proving groundbreaking 10X increase in design throughput.

"Imagination Technologies has been a key partner for Synopsys from the early release of IC Compiler II," said Bijan Kiani, vice president for the Design Group at Synopsys, Inc. "Our partners and mutual customers are benefitting from the game-changing aspects of IC Compiler II, not only to enhance their internal flows but also to deliver highly differentiated products to the market, demonstrating the transformational nature of this product."

About Synopsys

Synopsys, Inc. (Nasdaq:SNPS) is the Silicon to Software™ partner for innovative companies developing the electronic products and software applications we rely on every day. As the world's 15th largest software company, Synopsys has a long history of being a global leader in electronic design automation (EDA) and semiconductor IP, and is also a leader in software quality and security testing with its Coverity® solutions. Whether you're a system-on-chip (SoC) designer creating advanced semiconductors, or a software developer writing applications that require the highest quality and security, Synopsys has the solutions needed to deliver innovative, high-quality, secure products. Learn more at www.synopsys.com

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