Synopsys Enables Continuous Debug Innovation with More Than 200 VC Apps Now Available on the Verdi Platform

MOUNTAIN VIEW, Calif., March 17, 2015 /PRNewswire/ -- Synopsys, Inc. (Nasdaq:SNPS) now has more than 200 debug and analysis apps available on the VC Apps Exchange portal and in the Verdi® VC Apps Toolbox, demonstrating rapid momentum for customized applications that drive continuous innovation and further enable system-on-chip (SoC) teams to address their debug challenges. Synopsys' VC Apps provide direct access to the design, environment and verification information in Synopsys' Verdi open and extensible debug platform, which is widely used for all major simulation, emulation, formal and static verification flows. Through strong customer and third-party EDA company participation, the range of capabilities available as VC Apps has expanded to the areas of low power design analysis and enhanced debug collaboration.

"As a fabless semiconductor company developing our *advanced supercomputing on a chip™* technology, we need an efficient tool to manage the design and verification complexity inherent in many-core processor architectures," said Jehan-Philippe Barbiero, hardware verification manager at Kalray. "To meet these needs, we have used VC Apps as a common platform and have created several methodologies and apps to automate our verification and debug flows, including Smart Dumping Control App and Auto Waveform Manipulation App. As a result, we have seen noticeable improvement in the productivity of waveform generation. VC Apps has also helped us to share the knowhow of IPs and cores with all of the Kalray teams."

"Our Ascent and Meridian SoC verification and RTL sign-off software effectively handle the most complex challenges of design teams, including cross-domain crossing analysis," said Graham Bell, vice president of marketing at Real Intent. "Our native integration with the Verdi debug platform through VC Apps enables easy identification of design issues from within our debug environment and leads to quick resolution of any root causes."

With debug consuming as much as 50 percent of verification time and effort due to the complexities of today's designs, constant innovation and customization are needed. To make debug maximally efficient, VC Apps provide users with the tools to create custom applications to overcome their unique design and verification challenges. Through Synopsys' collaboration with customers and partners globally, the number of available VC Apps continues to grow. Now with more than 200 apps available, customers have access to capabilities that address many design challenges including design exploration, design rule validation, debug automation, debug waveform investigation, power estimation and exploration, technology integration and design manipulation. These customizations and automated work flows boost productivity and help design teams address their unique and changing verification challenges.

"To effectively address the verification challenges of complex SoCs, customized debug is necessary," said Yu-Chin Hsu, vice president of R&D in the Synopsys Verification Group. "Through close collaboration with industry leaders, we have made a significant investment in building the framework and assisting SoC teams in leveraging the strength of Verdi's open and extensible platform. With VC Apps, users can create custom applications to fulfill the ever-growing and evolving needs of their design and verification environments."

The VC Apps Developers Forum, focused on accelerating SoC debug and verification, will be held on Tuesday, March 24 from 10:30am – 12:00pm at the SNUG® Silicon Valley 2015 conference. At this forum, SoC design teams from around the world can learn from Synopsys and industry experts on how to create their own VC Apps and use them to boost verification and debug efficiency.

Availability

VC Apps are available today at the VC Apps Exchange, as well as through the VC Apps Toolbox in the general availability release of Verdi. These technologies are also included as part of Synopsys' Verification Compiler™ product.

About VC Apps

VC Apps – formerly known as Verdi Interoperable Apps (VIA) – is a programming interface that allows SoC design teams and EDA vendors to access information from Verdi's debug databases and analysis engines to innovate, seamlessly integrate and customize within the Verdi debug environment. The VC Apps Exchange website offers a variety of training materials, reference manuals and VC Apps examples. Learn more at www.vc-apps.org and attend an online introductory webinar. The Synopsys VC Apps Access Program provides qualified access to the Verdi product to enable interoperability in support of verification and design flows. For more information about the program, visit www.synopsys.com/vc-apps.

About Synopsys

Synopsys, Inc. (Nasdaq:SNPS) is the Silicon to Software™ partner for innovative companies developing the electronic

products and software applications we rely on every day. As the world's 15th largest software company, Synopsys has a long history of being a global leader in electronic design automation (EDA) and semiconductor IP, and is also a leader in software quality and security testing with its Coverity® solutions. Whether you're a system-on-chip (SoC) designer creating advanced semiconductors, or a software developer writing applications that require the highest quality and security, Synopsys has the solutions needed to deliver innovative, high-quality, secure products. Learn more at www.synopsys.com.

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Editorial Contacts:

Sheryl Gulizia
Synopsys, Inc.
650-584-8635
sgulizia@synopsys.com

Lisa Gillette-Martin
MCA, Inc.
650-968-8900, ext.115
lgmartin@mcapr.com

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