

Synopsys Surpasses Milestone of 100 VC Apps on the Verdi Debug Solution

MOUNTAIN VIEW, Calif., Dec. 15, 2014 /PRNewswire/ -- Synopsys, Inc. (Nasdaq:SNPS), a global leader providing software, IP and services used to accelerate innovation in chips and electronic systems, today announced that the open Verdi® debug solution has achieved the milestone of more than 100 debug and analysis apps developed through the VC Apps open interfaces. These apps are now available through the Verdi VC Apps ToolBox as well as the VC Apps Exchange website. Developed and contributed by Verdi users and third-party EDA companies, these apps offer access to additional debug and analysis capabilities for all Verdi users.

"As a leading automotive MCU provider delivering highly reliable and high-safety products, we place special priority on design and verification quality," said Tatsuji Kagatani, department manager of Design Automation Department, Renesas System Design. "Our verification environment includes several sophisticated methodologies, and we have used VC Apps to enhance these flows with several capabilities including efficient connectivity checks, fault coverage analysis and automated documentation. VC Apps have greatly helped us to proficiently drive improvement of design and verification quality."

"Addressing the challenges of system-on-chip (SoC) designs with one or more embedded processors, our Trek family of products enables design managers and verification engineers to realize measurable productivity gains, speed coverage closure and easily reuse verification knowledge," said Adnan Hamid, CEO at Breker Verification Systems. "With VC Apps, the Trek family of products is now fully integrated with the advanced Verdi debug solution, allowing our joint customers to identify the root cause of design issues and quickly resolve them."

With the increasing complexity of today's designs, debug now consumes as much as 50 percent of verification time and effort. To make design and verification more efficient, VC Apps provide users with capabilities to create custom applications to overcome complex design and verification challenges. Through Synopsys' collaboration with customers and partners globally, the number of VC Apps continues to grow and now exceeds 100, addressing many design challenges including design exploration, design rule validation, debug automation, debug waveform investigation, technology integration and design manipulation. These customizations and automated work flows boost productivity and in many cases eliminate manual methods.

"We have been collaborating closely with many SoC leaders to develop VC Apps that fit their design and verification environment, which is essential to effectively address the verification challenges of complex SoCs," said Yu-Chin Hsu, vice president of R&D in the Synopsys Verification Group. "We have made a significant investment in building the framework and opening the APIs to allow users to leverage the strength of Verdi. With VC Apps, users can customize applications to fulfill the many needs of their design and verification environments."

Availability

VC Apps are available today at [VC Apps Exchange](#), as well as through the VC Apps Toolbox in the general availability release of Verdi today. These technologies are also included as part of Synopsys' [Verification Compiler™](#) product.

About VC Apps

VC Apps – formerly known as Verdi Interoperable Apps (VIA) – is a programming interface that allows SoC design teams and EDA vendors to access information from Verdi's debug databases and analysis engines to innovate, seamlessly integrate and customize within the Verdi debug environment. The VC Apps Exchange website offers a variety of documents such as training materials, reference manual and VC Apps example apps. Learn more at www.vc-apps.org and attend an online introductory [webinar](#). The Synopsys VC Apps Access Program provides qualified access to the Verdi product to enable interoperability in support of verification and design flows. For more information about the program, visit www.synopsys.com/vc-apps.

About Synopsys

Synopsys, Inc. (NASDAQ:SNPS) accelerates innovation in the global electronics market. As a leader in electronic design automation (EDA) and semiconductor IP, its software, IP and services help engineers address their design, verification, system and manufacturing challenges. Since 1986, engineers around the world have been using Synopsys technology to design and create billions of chips and systems. Learn more at www.synopsys.com.

Editorial Contacts:

Sheryl Gulizia
Synopsys, Inc.
650-584-8635
sgulizia@synopsys.com

Lisa Gillette-Martin
MCA, Inc.
650-968-8900, ext.115
lgmartin@mcapr.com

SOURCE Synopsys, Inc.
