Synopsys' IC Validator Adopted by Plastic Logic for Physical Verification of Advanced Displays

Synopsys DRC and LVS Signoff Has Speed and Capacity to Handle Huge Flexible Screen Devices

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Highlights:

- Plastic Logic adopts Synopsys IC Validator for physical verification signoff of flexible display designs using proprietary organic thin film transistor technology
- IC Validator's hierarchical technology efficiently handles huge design sizes

Synopsys, Inc. (Nasdaq:SNPS), a global leader providing software, IP and services used to accelerate innovation in chips and electronic systems, and Plastic Logic, a world leader in plastic electronics, today announced that Plastic Logic has adopted Synopsys' IC Validator product for physical verification of Plastic Logic's proprietary organic thin film transistor technology. IC Validator's modern design and advanced hierarchical capabilities allowed it to easily accommodate Plastic Logic's unique process technology and deliver fast turn-around times on Plastic Logic's very large flexible screen designs.

"Plastic Logic has very large area semiconductor designs that require signoff verification of our proprietary technology," said Sebastian Mosler, design and layout engineer at Plastic Logic. "Synopsys is able to quickly deliver a runset that performs all of the design rule and connectivity checks we need. IC Validator can handle the often radically new design requirements that are enabled by a flexible transistor technology that can enable unique product designs and features."

Advances in process technology have placed growing demands on physical verification tools to check many more design rules that are also becoming much more complex. This evolution has created intense interest among IC designers for the latest and most capable physical verification tools that can address these new challenges. IC Validator is a comprehensive solution for all physical verification tasks, including design rule checks (DRC), layout-vs.-schematic (LVS) checks, electrical rule checking (ERC) and metal fill insertion. Its modern architecture and excellent multi-core scalability make IC Validator the signoff tool of choice for a growing number of customers—from those doing small analog designs to customers working on the most advanced digital chip designs in the world.

"We are committed to providing the best solutions for our customers working with leading-edge technologies including FinFET, silicon-on-insulator (SOI), and thin film transistors," said Bijan Kiani, vice president of product marketing, Design Group at Synopsys. "We look forward to further collaboration with Plastic Logic on solutions for the next generation of manufacturing technologies."

About Synopsys

Synopsys, Inc. (Nasdaq: SNPS) accelerates innovation in the global electronics market. As a leader in electronic design automation (EDA) and semiconductor IP, Synopsys delivers software, IP and services to help engineers address their design, verification, system and manufacturing challenges. Since 1986, engineers around the world have been using Synopsys technology to design and create billions of chips and systems. Learn more at http://www.synopsys.com.

About Plastic Logic

Plastic Logic is the leader in the development and commercialisation of plastic electronics. Our highly flexible, fully organic thin film transistor (OTFT) backplanes can be used to drive flex AMOLED displays, EPD displays and can also form the basis for large area flexible sensors. Displays using our technology have significant advantages over conventional silicon-based displays. Depending on the type of display, these will include: flexibility and conformability, shatterproof, lightweight, thin, low power consumption and daylight readable. Sensor applications include medical devices and sophisticated biometric security (fingerprint) to enable secure payment through mobile and other POS devices. Find out more about Plastic Logic and its robust, flexible displays by visiting http://www.plasticlogic.com and http://www.youtube.com/plasticlogic.

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